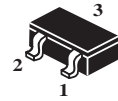
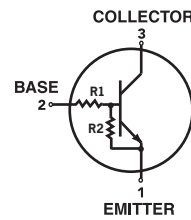


NPN Silicon Bias Resistor Transistor

 Lead(Pb)-Free



SC-59

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CE0}	50	Vdc
Collector-Base Voltage	V_{CB0}	50	Vdc
Collector Current-Continuous	I_C	100	mAdc

THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (1) $T_A=25^{\circ}\text{C}$ Derate above 25°C	P_D	230	mW
		1.8	$\text{mW}/^{\circ}\text{C}$
Thermal Resistance, Junction to Ambient ⁽¹⁾	$R_{\theta JA}$	540	$^{\circ}\text{C}/\text{W}$
Junction and Storage, Temperature	T_J, T_{stg}	-55 to +150	$^{\circ}\text{C}$

1. FR-4 @ minimum pad

Device Marking and Resistor Values

Device	Marking	R1(K)	R2(K)	Device	Marking	R1(K)	R2(K)
MUN2211	8A	10	10	MUN2232	8J	4.7	4.7
MUN2212	8B	22	22	MUN2233	8K	4.7	47
MUN2213	8C	47	47	MUN2234	8L	22	47
MUN2214	8D	10	47	MUN2236	8N	100	100
MUN2215	8E	10	∞	MUN2237	8P	47	22
MUN2216	8F	4.7	∞	MUN2240	8T	47	∞
MUN2230	8G	1.0	1.0	MUN2241	8U	100	∞
MUN2231	8H	2.2	2.2				

ELECTRICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector-Base Cutoff Current ($V_{CB} = 50\text{ V}$, $I_E = 0$)	I_{CBO}	-	-	100	nAdc
Collector-Emitter Cutoff Current ($V_{CE} = 50\text{ V}$, $I_B = 0$)	I_{CEO}	-	-	500	nAdc
Emitter-Base Cutoff Current ($V_{EB} = 6.0\text{ V}$, $I_C = 0$)	I_{EBO}	-	-	0.5	mAdc
MUN2211		-	-	0.2	
MUN2212		-	-	0.1	
MUN2213		-	-	0.2	
MUN2214		-	-	0.9	
MUN2215		-	-	1.9	
MUN2216		-	-	4.3	
MUN2230		-	-	2.3	
MUN2231		-	-	1.5	
MUN2232		-	-	0.18	
MUN2233		-	-	0.13	
MUN2234		-	-	0.05	
MUN2236		-	-	0.13	
MUN2237		-	-	0.2	
MUN2240		-	-	0.1	
MUN2241		-	-		
Collector-Base Breakdown Voltage ($I_C = 10\text{ }\mu\text{A}$, $I_E = 0$)	$V_{(BR)CBO}$	50	-	-	Vdc
Collector-Emitter Breakdown Voltage (1) ($I_C = 2.0\text{ mA}$, $I_B = 0$)	$V_{(BR)CEO}$	50	-	-	Vdc

ON CHARACTERISTICS (1)

DC Current Gain ($V_{CE} = 10\text{ V}$, $I_C = 5.0\text{ mA}$)	h_{FE}	35	60	-	
MUN2211		60	100	-	
MUN2212		80	140	-	
MUN2213		80	140	-	
MUN2214		160	350	-	
MUN2215		160	350	-	
MUN2216		3.0	5.0	-	
MUN2230		8.0	15	-	
MUN2231		15	30	-	
MUN2232		80	200	-	
MUN2233		80	150	-	
MUN2234		80	150	-	
MUN2236		80	140	-	
MUN2237		160	350	-	
MUN2240		160	350	-	
MUN2241					
Collector-Emitter Saturation Voltage ($I_C = 10\text{ mA}$, $I_B = 0.3\text{ mA}$) ($I_C = 10\text{ mA}$, $I_B = 5\text{ mA}$) MUN2230/MUN2231 ($I_C = 10\text{ mA}$, $I_B = 1\text{ mA}$) MUN2215/MUN2216 MUN2232/MUN2233/MUN2234	$V_{CE(sat)}$	-	-	0.25	Vdc
Output Voltage (on) ($V_{CC} = 5.0\text{ V}$, $V_B = 2.5\text{ V}$, $R_L = 1.0\text{ k}\Omega$)	V_{OL}	-	-	0.2	Vdc
MUN2211		-	-	0.2	
MUN2212		-	-	0.2	
MUN2214		-	-	0.2	
MUN2215		-	-	0.2	
MUN2216		-	-	0.2	
MUN2230		-	-	0.2	
MUN2231		-	-	0.2	
MUN2232		-	-	0.2	
MUN2233		-	-	0.2	
MUN2234		-	-	0.2	
($V_{CC} = 5.0\text{ V}$, $V_B = 3.5\text{ V}$, $R_L = 1.0\text{ k}\Omega$)		-	-	0.2	
MUN2213		-	-	0.2	
MUN2240		-	-	0.2	
($V_{CC} = 5.0\text{ V}$, $V_B = 5.5\text{ V}$, $R_L = 1.0\text{ k}\Omega$)		-	-	0.2	
MUN2236		-	-	0.2	
($V_{CC} = 5.0\text{ V}$, $V_B = 4.0\text{ V}$, $R_L = 1.0\text{ k}\Omega$)		-	-	0.2	
MUN2237		-	-	0.2	
($V_{CC} = 5.0\text{ V}$, $V_B = 5.0\text{ V}$, $R_L = 1.0\text{ k}\Omega$)		-	-	0.2	
MUN2241		-	-	0.2	

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Typ	Max	Unit
ON CHARACTERISTICS (Note 1) (Continued)					
Output Voltage (of f) ($V_{CC} = 5.0\text{V}$, $V_B = 0.5\text{V}$, $R_L = 1.0\text{k}\Omega$) $(V_{CC} = 5.0\text{V}$, $V_B = 0.050\text{V}$, $R_L = 1.0\text{k}\Omega$) $(V_{CC} = 5.0\text{V}$, $V_B = 0.25\text{V}$, $R_L = 1.0\text{k}\Omega$)	V_{OH}	4.9	-	-	Vdc
Input Resistor	R_1				$\text{k}\Omega$
MUN2211		7.0	10	13	
MUN2212		15.4	22	28.6	
MUN2213		32.9	47	61.1	
MUN2214		7.0	10	13	
MUN2215		7.0	10	13	
MUN2216		3.3	4.7	6.1	
MUN2230		0.7	1.0	1.3	
MUN2231		1.5	2.2	2.9	
MUN2232		3.3	4.7	6.1	
MUN2233		3.3	4.7	6.1	
MUN2234		15.4	22	28.6	
MUN2235		70	100	130	
MUN2236		32.9	47	61.1	
MUN2237		70	100	130	
MUN2240		32.9	47	61.1	
MUN2241		70	100	100	
Resistor Ratio	R_1/R_2				
MUN2211/MUN2212/MUN2213 MUN2236		0.8	1.0	1.2	
MUN2214		0.17	0.21	0.25	
MUN2215/MUN2216/MUN2240 MUN2241		-	-	-	
MUN2230/MUN2231/MUN2232		0.8	1.0	1.2	
MUN2233		0.055	0.1	0.185	
MUN2234		0.38	0.47	0.56	
MUN2237		1.7	2.1	2.6	

1. Pulse Test: Pulse Width < 300 ms, Duty Cycle < 2.0%

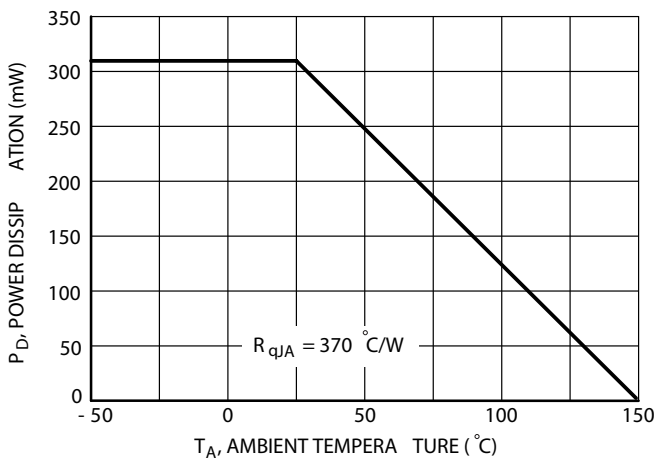


FIG 1. Derating Curve

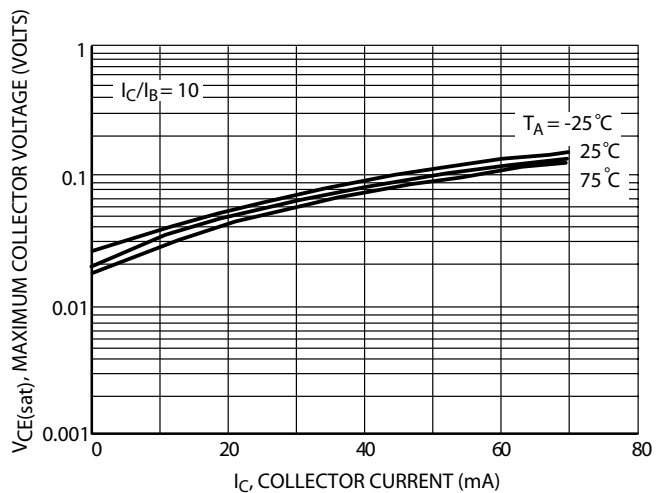


FIG2. $V_{CE(sat)}$ versus I_C

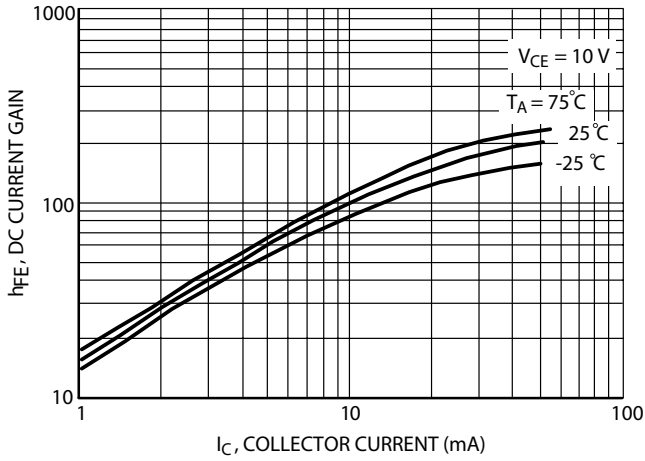


FIG3. DC Current Gain

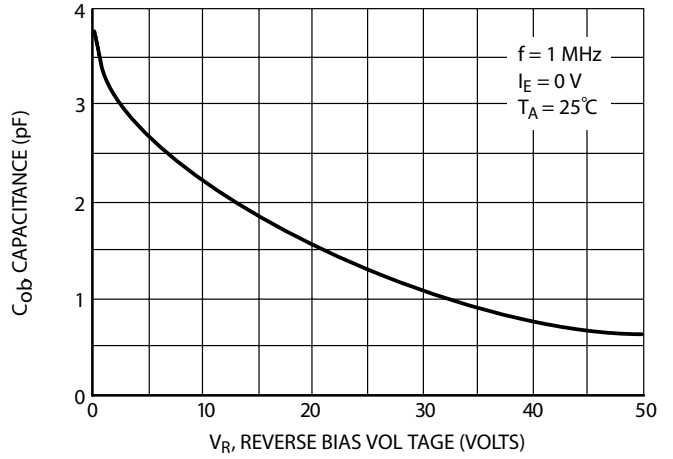


FIG4. Output Capacitance

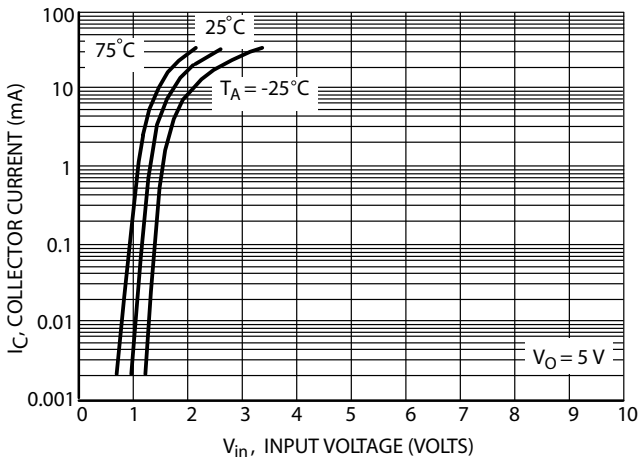


FIG5. Output Current versus Input Voltage

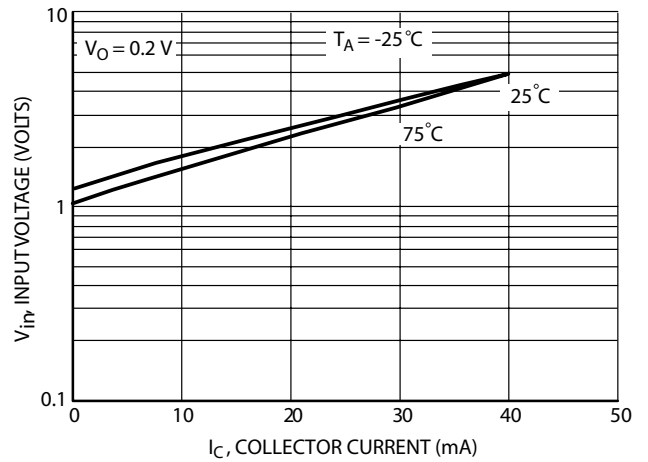


FIG6. Input Voltage versus Output Current

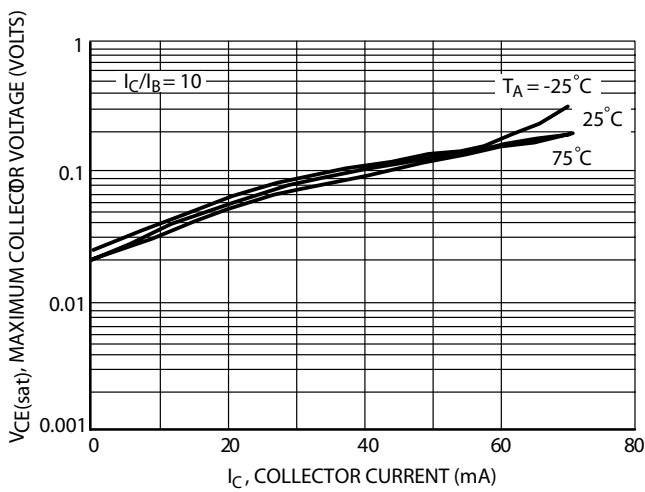


FIG7. $V_{CE(sat)}$ versus I_C

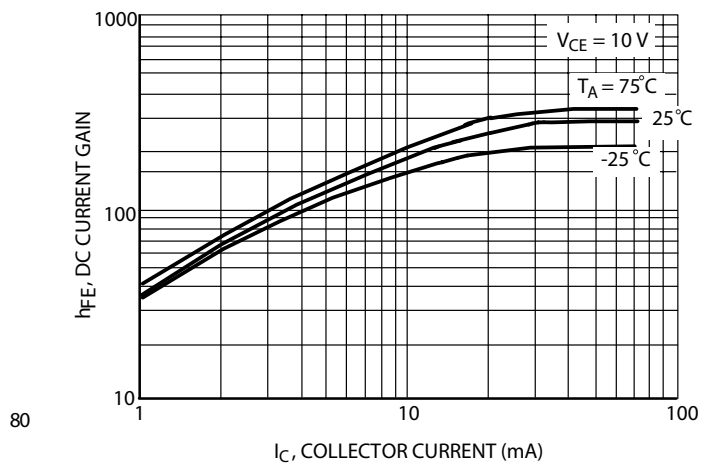


FIG8. DC Current Gain

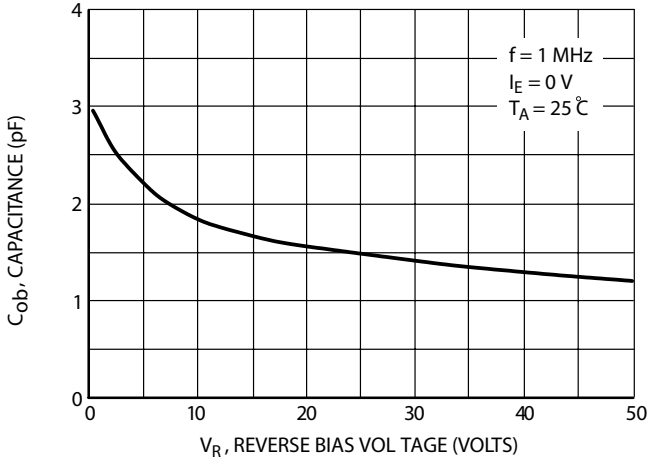


FIG9. Output Capacitance

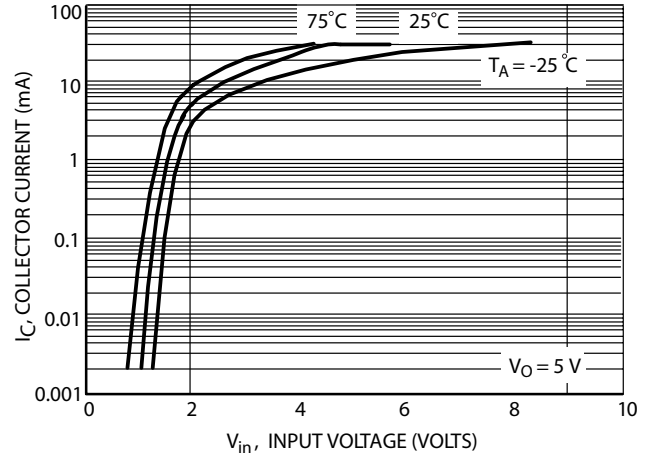


FIG10. Output Current versus Input Voltage

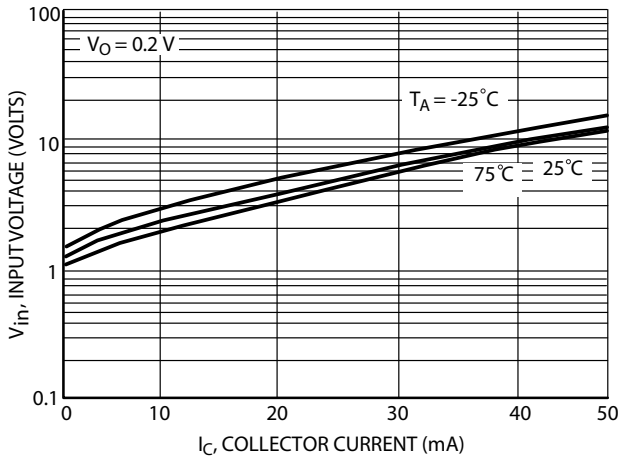


FIG11. Input Voltage versus Output Current

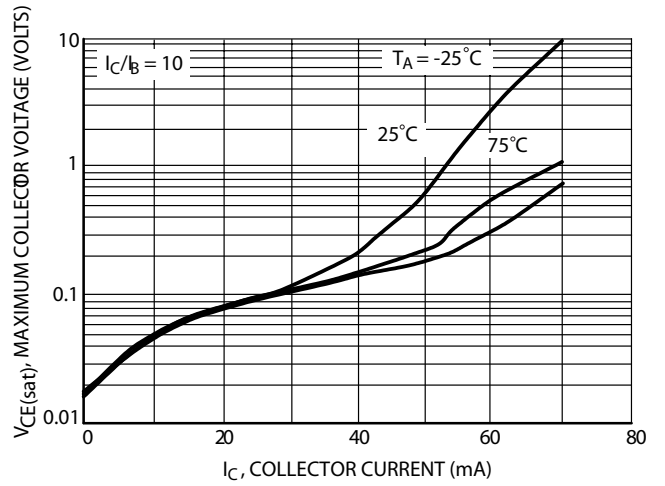


FIG12. $V_{CE(sat)}$ versus I_C

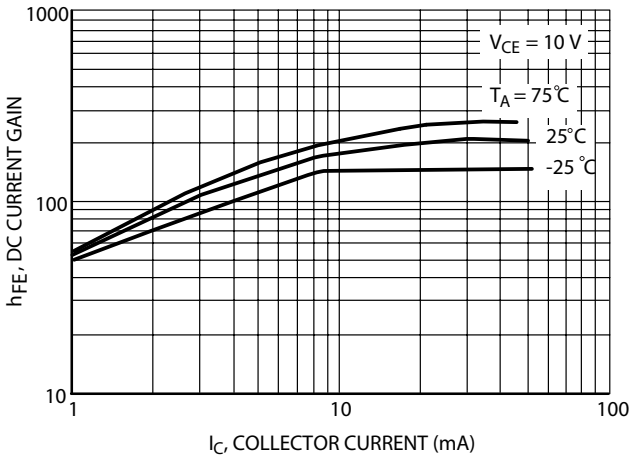


FIG13. DC Current Gain

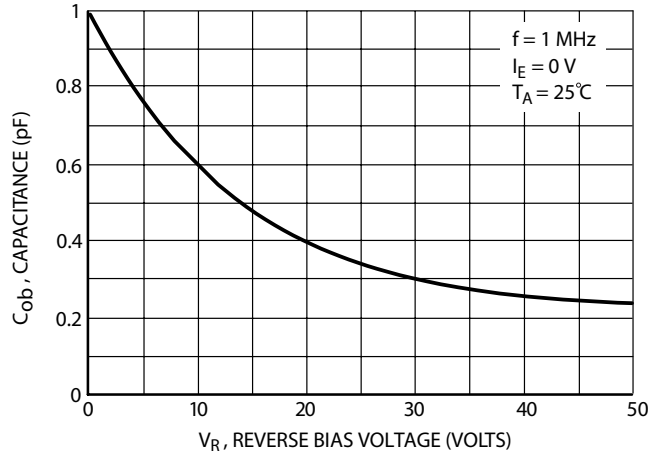


FIG14. Output Capacitance

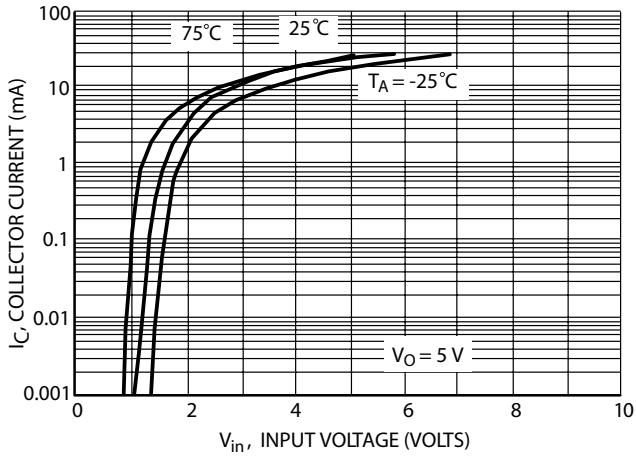


FIG15. Output Current versus Input Voltage

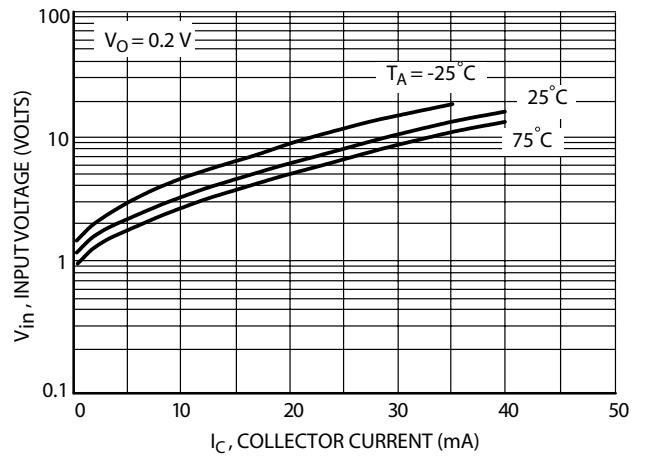


FIG16. Input Voltage versus Output Current

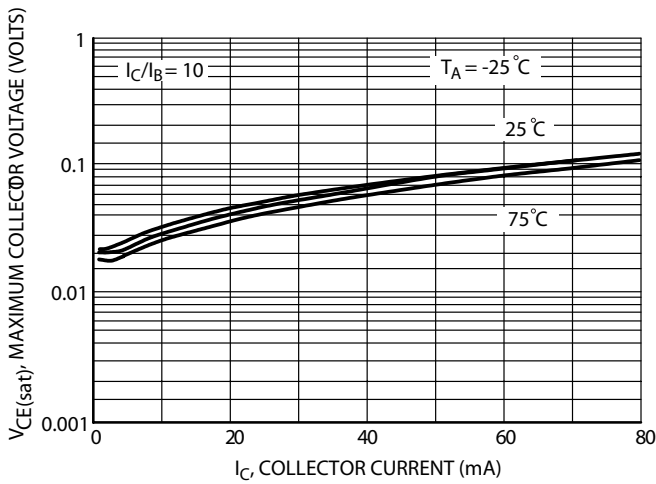


FIG17. $V_{CE(sat)}$ versus I_C

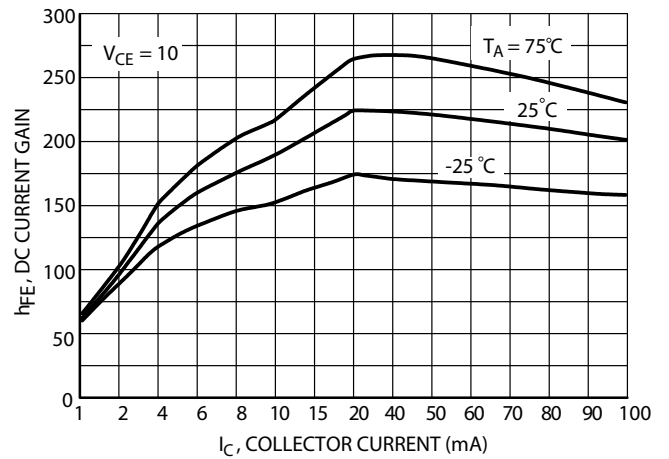


FIG18. DC Current Gain

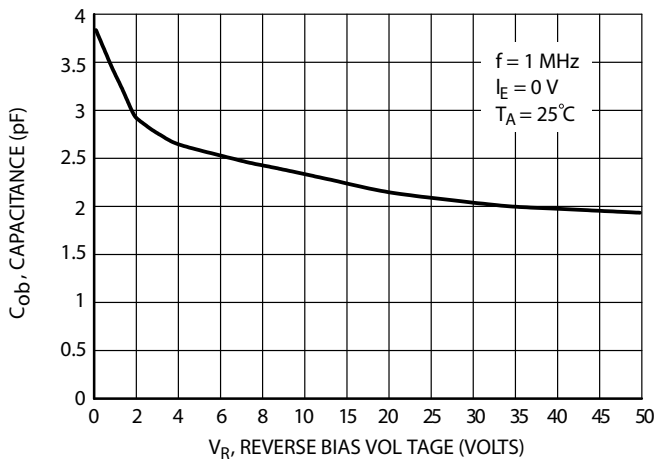


FIG19. Output Capacitance

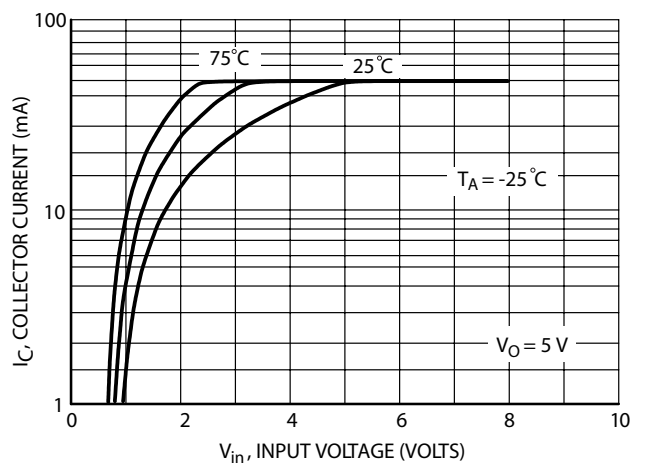


FIG20. Output Current versus Input Voltage

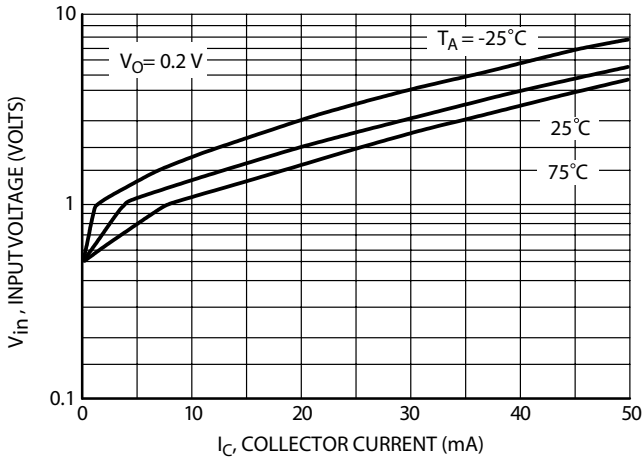


FIG21. Input V voltage versus Output Current

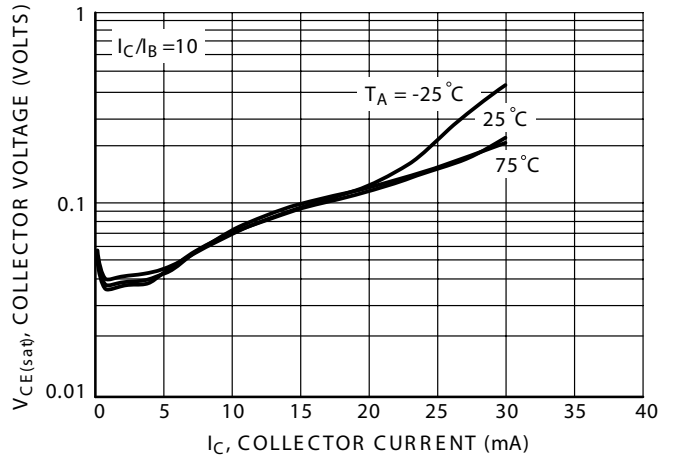


FIG22. $V_{CE(sat)}$ versus I_C

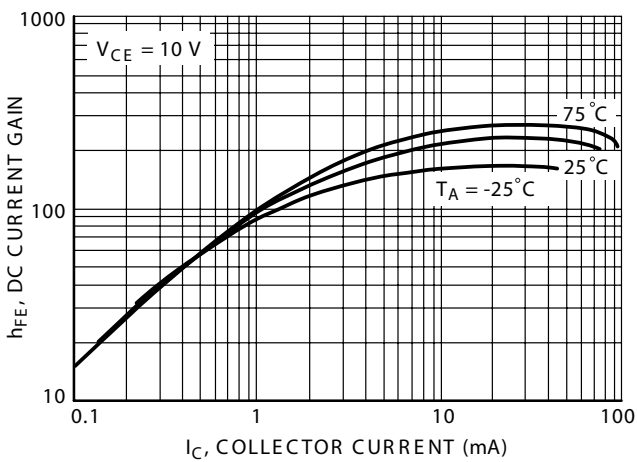


FIG23. DC Current Gain

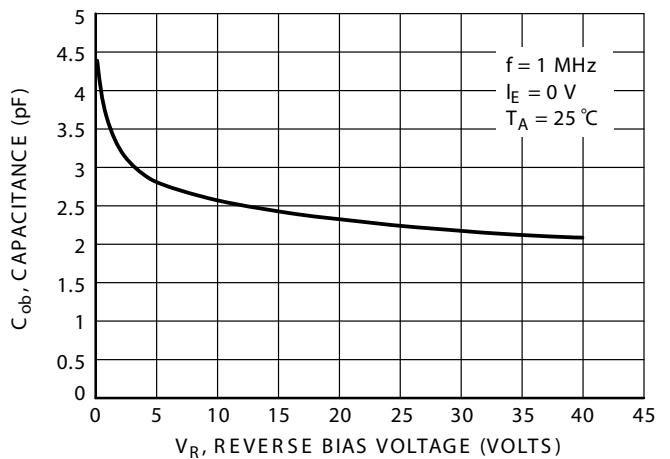


FIG24. Output Capacitance

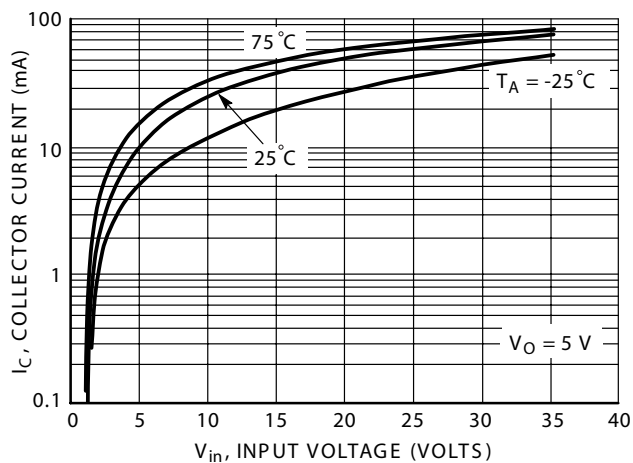


FIG25. Output Current versus Input Voltage

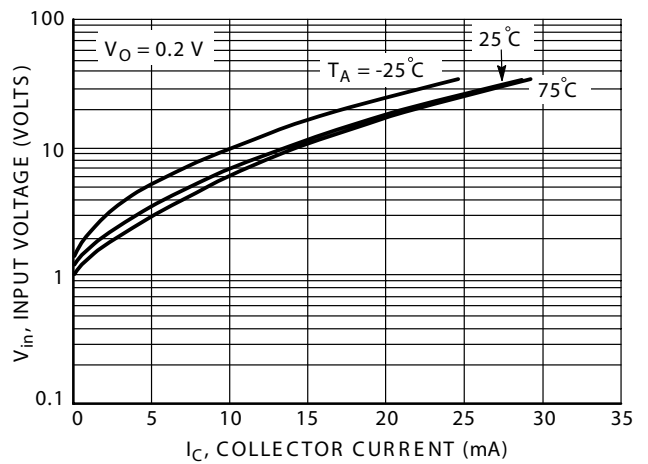


FIG26. Input Voltage versus Output Current

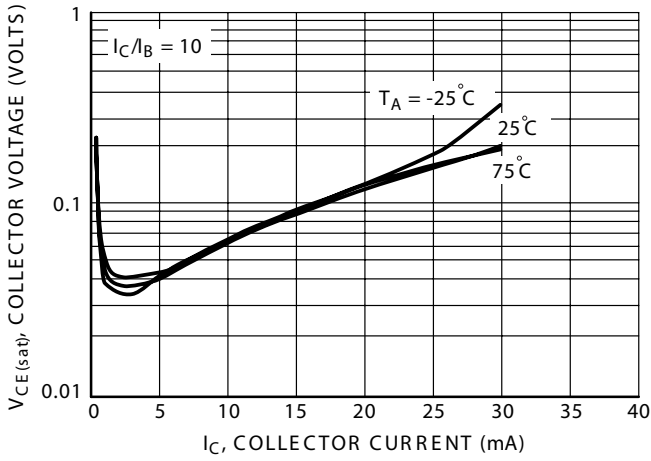


FIG27. $V_{CE(sat)}$ versus I_C

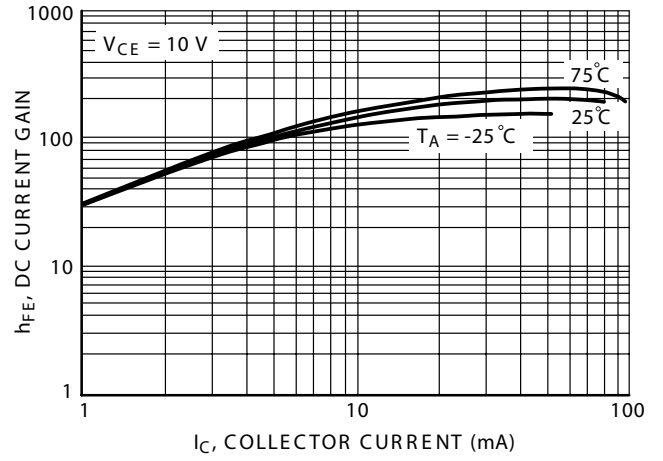


FIG28. DC Current Gain

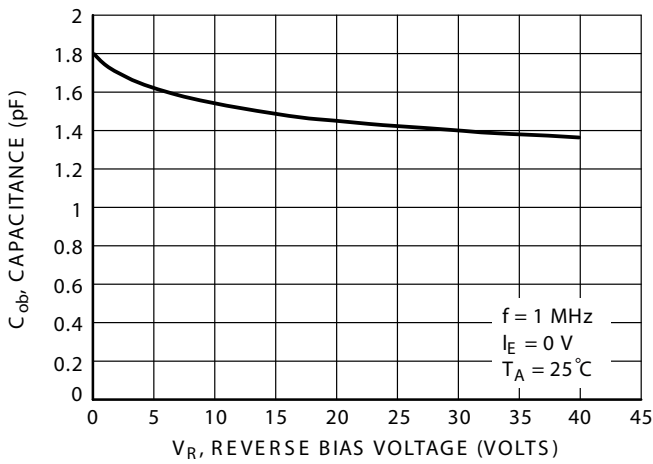


FIG29. Output Capacitance

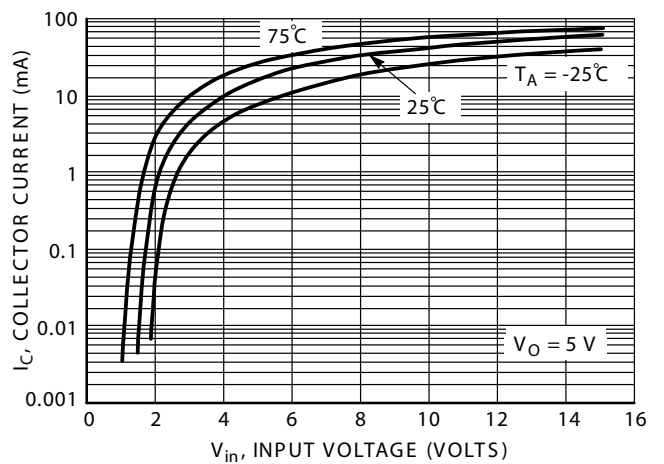


FIG30. Output Current versus Input Voltage

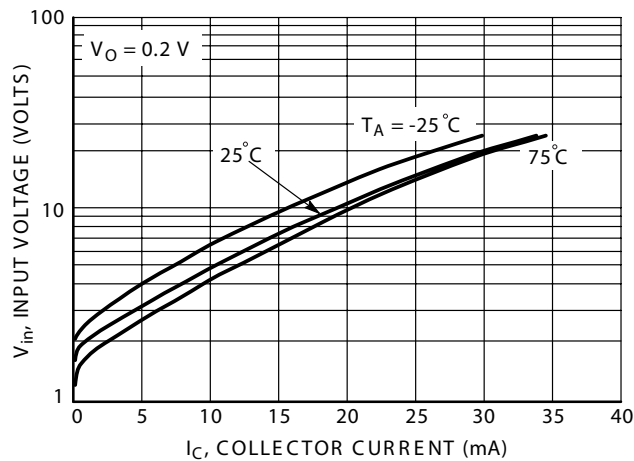


FIG31. Input Voltage versus Output Current

TYPICAL APPLICATIONS FOR NPN BJT's

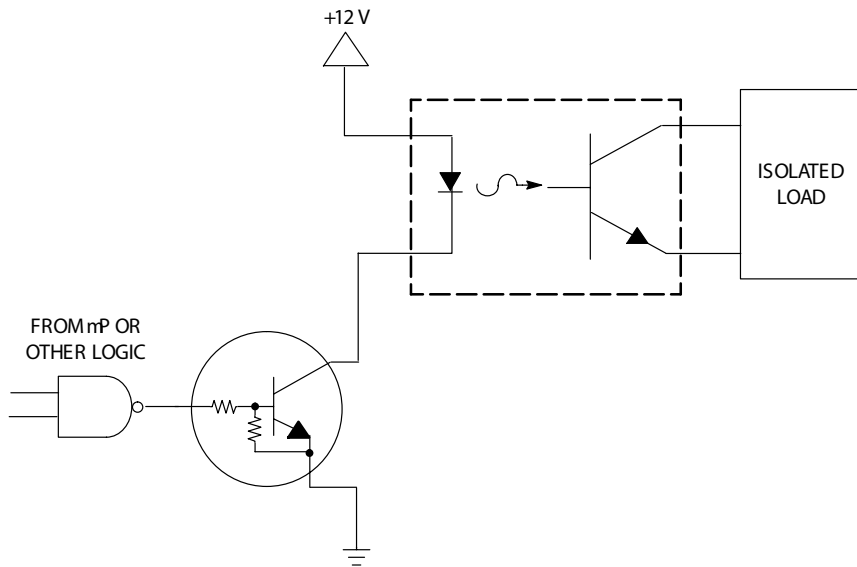
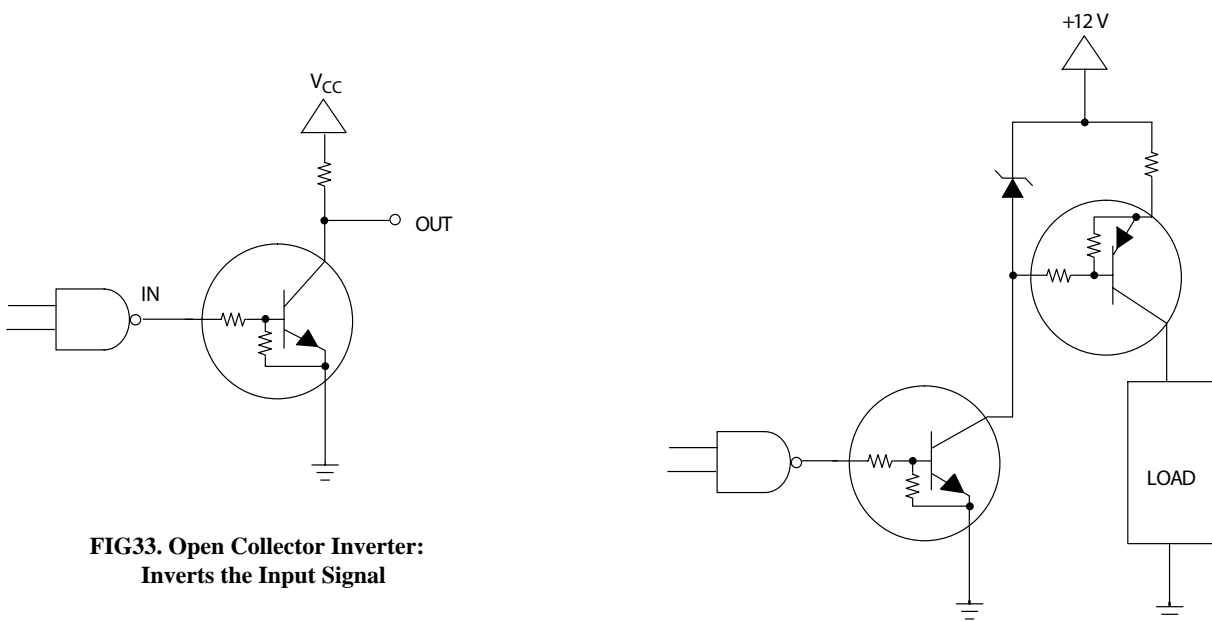


FIG32. Level Shifter: Connects 12 or 24 Volt Circuits to Logic



**FIG33. Open Collector Inverter:
Inverts the Input Signal**

FIG34. Inexpensive, Unregulated Current Source

SC-59 Outline Dimension

