

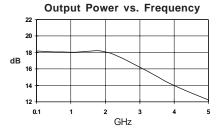
# **Product Description**

Stanford Microdevices' SNA-500 is a GaAs monolithic broadband amplifier in die form. This amplifier provides 19dB of gain when biased at 70mA and 5.0V. P1dB and TOIP may be improved by 2dB by baising @ 100mA.

External DC decoupling capacitors determine low frequency response. The use of an external resistor allows for bias flexibility and stability.

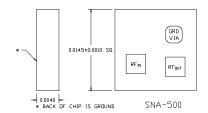
These unconditionally stable amplifiers are designed for use as general purpose 50 ohm gain blocks. Also available in packaged form (SNA-576, -586 & -587), its small size (0.4mm x 0.4mm) and gold metallization make it an ideal choice for use in hybrid circuits.

The SNA-500 is available in gel paks at 100 devices per container.



# **SNA-500**

# DC-3 GHz, Cascadable GaAs MMIC Amplifier



## **Product Features**

- Cascadable 50 Ohm Gain Block
- 19dB Gain, +18dBm P1dB
- 1.5:1 Input and Output VSWR
- Operates From Single Supply
- Chip Back Is Ground

# **Applications**

- Narrow and Broadband Linear Amplifiers
- Commercial and Industrial Applications

### Electrical Specifications at Ta = 25C

Symbol	Parameters: Test Conditions: Id = 70 m A, Z <sub>o</sub> = 50 0 hm s		Units	M in.	Тур.	Мах.
G <sub>P</sub>	Small Signal Power Gain	f = 0.1-1.0 G H z f = 1.0-2.0 G H z f = 2.0-3.0 G H z	d B d B d B	18.0 16.0 15.0	2 0 .0 1 8 .0 1 7 .0	
G <sub>F</sub>	Gain Flatness	f = 0.1-2.0 GHz	d B		+/- 1.0	
B W 3 d B	3dB Bandwidth		G H z		3.0	
P <sub>1 d B</sub>	Output Power at 1dB Compression	f = 2.0 G H z	d B m		18.0	
ΝF	Noise Figure	f = 2.0 G H z	d B		4.2	5.0
V S W R	Input / Output	f = 0.1-8.0 GHz			1.5:1	
IP <sub>3</sub>	Third Order Intercept Point	f = 2.0 G H z	d B m		3 4 . 0	
T <sub>D</sub>	Group Delay	f = 2.0 G H z	psec		120	
ISOL	Reverse Isolation	f = 0.1-8.0 G H z	d B		22.0	
V D	Device Voltage		V	4.3	5.0	5 .7
d G /d T	Device Gain Temperature Coefficient		d B /d e g C		-0.0027	
dV/dT	Device Voltage Temperature Coefficient		m V/degC		-5.0	

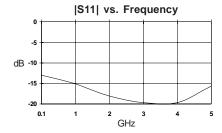
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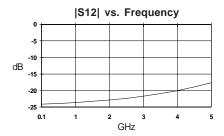
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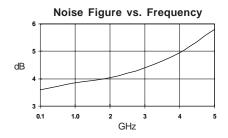


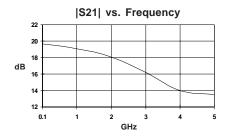
# SNA-500 DC-3 GHz Cascadable MMIC Amplifier

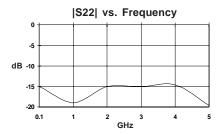
# Typical Performance at 25° C (Vds = 5.0V, Ids = 70mA)

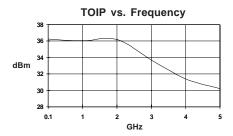












Typical Chip S-Parameters Vds = 5.0V, Ids = 70mA

Freq GHz	S11	S11 Ang	S21	S21 Ang	S12	S12 Ang	S22	S22 Ang
.100	0.162	149	10.39	175	0.059	-4	0.128	-176
.250	0.158	152	10.42	169	0.062	0	0.132	-165
.500	0.151	163	10.49	163	0.068	2	0.141	-160
1.00	0.147	175	10.53	159	0.074	4	0.152	-151
1.50	0.140	178	10.61	146	0.076	6	0.204	-148
2.00	0.129	-178	10.64	137	0.077	8	0.243	-149
2.50	0.125	-167	10.54	122	0.079	10	0.293	-155
3.00	0.136	-156	10.25	111	0.082	11	0.323	-160

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## **Absolute Maximum Ratings**

Parameter	Absolute Maximum		
Device Current	130m A		
Power Dissipation	750mW		
RF Input Power	200mW		
Junction Temperature	+200C		
Operating Temperature	-45C to +85C		
Storage Temperature	-65C to +150C		

#### Notes:

 Operation of this device above any one of these parameters may cause permanent damage.

## MTTF vs. Temperature @ Id = 70mA

Die Bottom Temperature	Junction Temperature	MTTF (hrs)
+65C	+155C	1000000
+100C	+190C	100000
+130C	+220C	10000

Thermal Resistance (Lead-Junction): 265° C/W

#### Die Attach

The die attach process mechanically attaches the die to the circuit substrate. In addition, it electrically connects the ground to the trace on which the die is mounted and establishes the thermal path by which heat can leave the die.

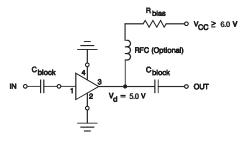
## **Assembly Techniques**

Epoxy die attach is recommended. The top and bottom metallization is gold. Conductive silver-filled epoxies are recommended. This method involves the use of epoxy to form a joint between the backside gold of the chip and the metallized area of the substrate. A 150 C cure for 1 hour is necessary. Recommended epoxy is Ablebond 84-1LMIT1 from Ablestik.

## SNA-500 DC-3 GHz Cascadable MMIC Amplifier

#### Part Number Ordering Information

Part Number	Devices Per Pak		
SNA-500	100		



**Typical Biasing Configuration** 

#### Wire Bonding

Electrical connections to the die are through wire bonds. Stanford Microdevices recommends wedge bonding or ball bonding to the pads of these devices.

#### Recommended Wedge Bonding Procedure

- 1. Set the heater block temperature to 260C +/- 10C.
- 2. Use pre-stressed (annealed) gold wire between 0.0005 to 0.001 inches in diameter.
- 3. Tip bonding pressure should be between 15 and 20 grams and should not exceed 20 grams. The footprint that the wedge leaves on the gold wire should be between 1.5 and 2.5 wire diameters across for a good bond.

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