STRUCTURE Silicon Monolithic Integrated Circuit

PRODUCT NAME γ-correction IC for TFT-LCD Panel

TYPE **BD8132FV**

FEATURES Built-in 10bit X 18 Channel γ-correction outputs
Built-in 10bit X 1 Channel common amplifier

●ABSOLUTE MAXIMUM RATINGS (Ta=25℃)

PARAMETER	SYMBOL	LIMITS	UNIT
Power Supply Voltage 1	DVCC	7	V
Power Supply Voltage 2	VCC	20	V
REFIN Voltage	REF	20	V
Amplifier Drive Current	Io	50*1	mA
Junction Temperature	Tjmax	150	℃
Power Dissipation	Pd	1125*2	mW
Operating Temperature Range	Topr	-30～+85	℃
Storage Temperature Range	Tstg	-55～+150	℃

*1 Do not however exceed Pd.
*2 Pd decreased at 9mV/℃ for temperatures above Ta=25℃, mounted on 70×70×1.6mm Glass-epoxy PCB.

●OPERATING CONDITIONS (Ta=-30℃～+85℃)

PARAMETER	SYMBOL	MIN	MAX	UNIT
Power Supply Voltage 1	DVCC	2.3	4.0	V
Power Supply Voltage 2	VCC	6	18	V
REFIN Voltage	REF	6	18	V
Amplifier Drive Current	Io	-	40	mA
Serial CLK Frequency	fCLK	-	5	MHZ
OSC Frequency	fosc	10	200	KHz

★This product is not designed for protection against radioactive rays.
★The product described in this specification is a strategic product(and/or Service) subject to COCOM regulations. It should not be exported without Authorization from the appropriate government.

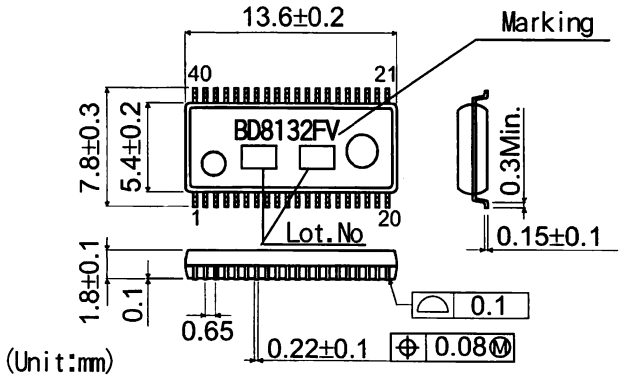
Status of this document
The Japanese version of this document is the formal specification.
A customer may use this translation version only for a reference to help reading the formal version.
If there are any differences in translation version of this document, formal version takes priority.

●ELECTRICAL CHARACTERISTICS (Unless otherwise specified VCC=15V, DVCC=3.3V, Ta=25℃)

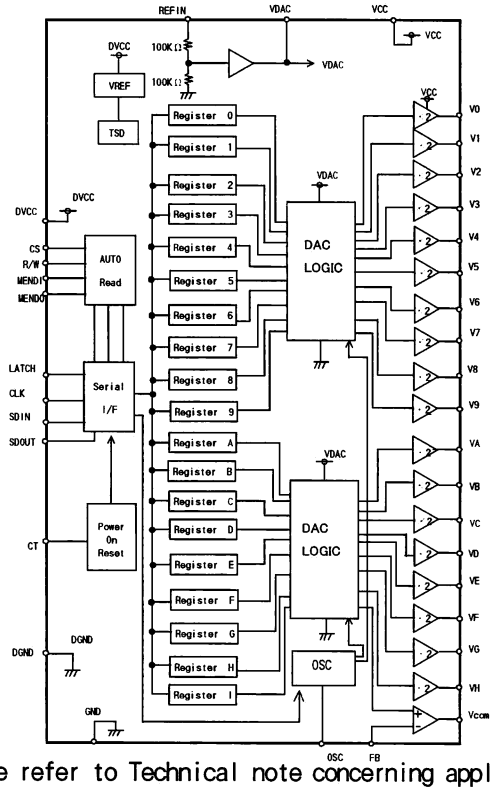
PARAMETER	SYMBOL	LIMIT			UNIT	CONDITIONS
		MIN	TYP	MAX		
【 REFIN 】						
Sink Current	IREF	25	50	75	μA	REF=10V
【 γ-CORRECTION AMP】						
Drive Current	Io	150	300	-	mA	DAC=3V, OUTx=0V
Load Regulation	ΔV	-	5	20	mV	Io=+10mA ～ -10mA, OUTx=6V
Slew Rate	SR	-	3.5	-	V/uS	Ro=100KΩ, Co=100pF ※
Output Voltage High	VOH	VCC-0.16	VCC-0.1	-	V	Io=-5mA
Output Voltage Low	VOL	-	0.15	0.24	V	Io=5mA
【 COMMON AMP 】						
Input Bias Current	Ib	-	0	1	μA	VFB=6V
Drive Current	Io	150	300	-	mA	DAC=3V, OUTx=0V
Load Regulation	ΔV	-	5	20	mV	Io=+10mA ～ -10mA, OUTx=3V
Slew Rate	SR	-	3.5	-	V/uS	Ro=100KΩ, Co=100pF ※
Input Voltage Range	VFB	0	-	VDAC	V	Ro=100KΩ, Co=100pF ※
Output Voltage high	VOH	VCC-0.16	VCC-0.1	-	V	Io=-5mA
Output voltage Low	VOL	-	0.15	0.24	V	Io=5mA
【 DAC 】						
Resolution Coding	Res	-	10	-	Bit	
Non-Linear Error	LE	-2	-	2	LSB	Error with ideal straight Range 00A～3F5
Differential Error	DLE	-2	-	2	LSB	Error with ideal amount of Increase in 1LSB Range 00A～3F5
【 OSC 】						
Frequency	fosc	-	80	-	KHz	Internal oscillator mode
【 CONTROL SIGNAL 】						
Sink Current	IctL	-	16	25	uA	
Threshold Voltage	VTH	0.7	-	2.6	V	DVCC=3.3V
Reset Time	trst	-	45	-	μs	CCT=1000pF
【 WHOLE DEVICE 】						
Circuit Current	Icc	-	20	-	mA	All outputs = 5V

※Design Guarantee (Outgoing inspection is not done all products.)

●PHYSICAL DIMENSION・MARKING (SSOP-B40)



●BLOCK DIAGRAM



*Please refer to Technical note concerning application circuit, and etc.

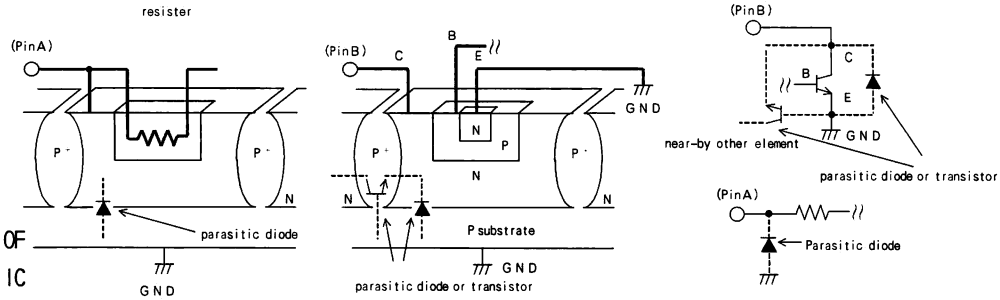
●PIN NO. & FUNCTION TABLE

PIN No	PIN NAME	Function	PIN No	PIN NAME	Function
1	LATCH	LATCH signal input	21	FB	COM -input
2	SDIN	DATA signal input	22	Vcom	COM output
3	CLK	CLK signal input	23	VH	Gamma H output
4	SDOUT	DATA signal output	24	VG	Gamma G output
5	GND	Ground	25	VF	Gamma F output
6	R/W	Automatic Read ON/OFF input (ON=L, OFF=H)	26	VE	Gamma E output
7	CS	Chip Select signal output	27	VD	Gamma D output
8	MEMDO	External Memory DATA output	28	VC	Gamma C output
9	MEMDI	External Memory DATA input	29	VB	Gamma B output
10	OSC	Synchronized clock inout	30	VA	Gamma A output
11	DVCC	Digital Power Supply	31	V9	Gamma 9 output
12	NC	—	32	V8	Gamma 8 output
13	VCC	Power Supply for Gamma 0~9	33	V7	Gamma 7 output
14	VCC	Power Supply for Gamma A~H, COM, REG	34	V6	Gamma 6 output
15	REFIN	DAC reference input	35	V5	Gamma 5 output
16	VDAC	DAC Voltage output	36	V4	Gamma 4 output
17	CT	Capacitor connection for Power on Reset	37	V3	Gamma 3 output
18	DGND	Ground for DAC	38	V2	Gamma 2 output
19	GND	Ground	39	V1	Gamma 1 output
20	GND	Ground	40	V0	Gamma 0 output

●Operation Notes

1. Absolute maximum range
   This product are produced with strict quality control, but might be destroyed in using beyond absolute maximum ratings. Open IC destroyed a failure mode cannot be defined (like Short mode, or Open mode). Therefore physical security countermeasure, like fuse, is to be given when a specified mode to be beyond absolute maximum ratings is considered.
2. Ground potential
   GND terminal should be a lowest voltage potential every state.
   Please make sure all pins which is over ground even if include transient feature.
3. Setting of heat
   Use a setting of heat that allows for a sufficient margin in light of the power dissipation (Pd) in actual operating conditions..
4. Short Circuit between Terminal and Soldering
   Don' t short-circuit between Output pin and V_{DD} pin, Output pin and GND pin, or V_{DD} pin and GND pin. When soldering the IC on circuit board, please be unusually cautious about the orientation and the position of the IC. When the orientation is mistaken the IC may be destroyed.
5. Electromagnetic Field
   Mal-function may happen when the device is used in the strong electromagnetic field.
6. Ground wiring patterns
   When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the application's reference point so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring patterns of any external components.
7. This IC is a monolithic IC which has P+ isolation in the P substrate and between the various pins.
   A P-N junction is formed from this P layer and the N layer of each pin.
   For example, when a resistor and a transistor is connected to a pin.
   Parasitic diodes can occur inevitably in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits as well as operation faults and physical damage. Accordingly, you must not use methods by which parasitic diodes operate, such as applying a voltage that is lower than the GND (P substrate) voltage to an input pin.

SIMPLIFIED STRUCTURE OF BI-POLAR IC



8. Over current protection circuit
   The over-current protection circuits are built in at output, according to their respective current outputs and prevent the IC from being damaged when the load is short-circuited or over-current. But, these protection circuits are effective for preventing destruction by unexpected accident. When it' s in continuous protection circuit moving period don' t use please. And for ability, because this chip has minus characteristic, be careful for heat plan.
9. Built-in thermal circuit
   A temperature control circuit is built in the IC to prevent the damage due to overheat.
   Therefore, all the outputs are turned off when the thermal circuit works and are turned on when the temperature goes down to the specified level.
10. Testing on application boards
    When testing the IC on an application board, connecting a capacitor to a pin with low impedance subjects the IC to stress. Always discharge capacitors after each process or step. Ground the IC during assembly steps as an antistatic measure, and use similar caution when transporting or storing the IC. Always turn the IC's power supply off before connecting it to or removing it from a jig or fixture during the inspection process.

Notes

- No technical content pages of this document may be reproduced in any form or transmitted by any means without prior permission of ROHM CO.,LTD.
- The contents described herein are subject to change without notice. The specifications for the product described in this document are for reference only. Upon actual use, therefore, please request that specifications to be separately delivered.
- Application circuit diagrams and circuit constants contained herein are shown as examples of standard use and operation. Please pay careful attention to the peripheral conditions when designing circuits and deciding upon circuit constants in the set.
- Any data, including, but not limited to application circuit diagrams information, described herein are intended only as illustrations of such devices and not as the specifications for such devices. ROHM CO.,LTD. disclaims any warranty that any use of such devices shall be free from infringement of any third party's intellectual property rights or other proprietary rights, and further, assumes no liability of whatsoever nature in the event of any such infringement, or arising from or connected with or related to the use of such devices.
- Upon the sale of any such devices, other than for buyer's right to use such devices itself, resell or otherwise dispose of the same, no express or implied right or license to practice or commercially exploit any intellectual property rights or other proprietary rights owned or controlled by
- ROHM CO., LTD. is granted to any such buyer.
- Products listed in this document are no antiradiation design.

The products listed in this document are designed to be used with ordinary electronic equipment or devices (such as audio visual equipment, office-automation equipment, communications devices, electrical appliances and electronic toys).
Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of with would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

About Export Control Order in Japan

Products described herein are the objects of controlled goods in Annex 1 (Item 16) of Export Trade Control Order in Japan.
In case of export from Japan, please confirm if it applies to "objective" criteria or an "informed" (by MITI clause) on the basis of "catch all controls for Non-Proliferation of Weapons of Mass Destruction.

Thank you for your accessing to ROHM product informations.

More detail product informations and catalogs are available,

please contact your nearest sales office.

Please contact our sales offices for details ;

U.S.A / San Diego	TEL : +1(858)625-3630	FAX : +1(858)625-3670
Atlanta	TEL : +1(770)754-5972	FAX : +1(770)754-0691
Dallas	TEL : +1(972)312-8818	FAX : +1(972)312-0330
Germany / Dusseldorf	TEL : +49(2154)9210	FAX : +49(2154)921400
United Kingdom / London	TEL : +44(1)908-282-666	FAX : +44(1)908-282-528
France / Paris	TEL : +33(0)1 56 97 30 60	FAX : +33(0) 1 56 97 30 80
China / Hong Kong	TEL : +852(2)740-6262	FAX : +852(2)375-8971
Shanghai	TEL : +86(21)6279-2727	FAX : +86(21)6247-2066
Dilian	TEL : +86(411)8230-8549	FAX : +86(411)8230-8537
Beijing	TEL : +86(10)8525-2483	FAX : +86(10)8525-2489
Taiwan / Taipei	TEL : +866(2)2500-6956	FAX : +866(2)2503-2869
Korea / Seoul	TEL : +82(2)8182-700	FAX : +82(2)8182-715
Singapore	TEL : +65-6332-2322	FAX : +65-6332-5662
Malaysia / Kuala Lumpur	TEL : +60(3)7958-8355	FAX : +60(3)7958-8377
Philippines / Manila	TEL : +63(2)807-6872	FAX : +63(2)809-1422
Thailand / Bangkok	TEL : +66(2)254-4890	FAX : +66(2)256-6334

Japan /
(Internal Sales)

Tokyo 2-1-1, Yaesu, Chuo-ku, Tokyo 104-0082
TEL : +81(3)5203-0321 FAX : +81(3)5203-0300

Yokohama 2-4-8, Shin Yokohama, Kohoku-ku, Yokohama, Kanagawa 222-8575
TEL : +81(45)476-2131 FAX : +81(45)476-2128

Nagoya Dainagayo Building 9F 3-28-12, Meieki, Nakamura-ku, Nagoya,Aichi 450-0002
TEL : +81(52)581-8521 FAX : +81(52)561-2173

Kyoto 579-32 Higashi Shiokouji-cho, Karasuma Nishi-iru, Shiokoujidori, Shimogyo-ku, Kyoto 600-8216
TEL : +81(75)311-2121 FAX : +81(75)314-6559

(Contact address for overseas customers in Japan)

Yokohama TEL : +81(45)476-9270 FAX : +81(045)476-9271

As of 18th. April 2005