# FOR LOW FREQUENCY AMPLIFY APPLICATION P CHANNEL JUNCTION TYPE

### DESCRIPTION

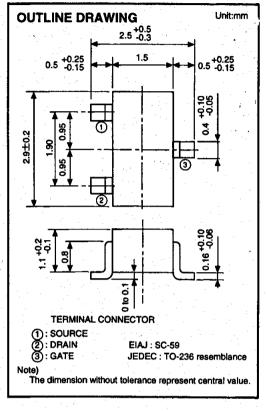
2SJ125 is a small type resin sealed P channel junction type FET. It is especially designed for low frequency voltage amplify, analog switch application.

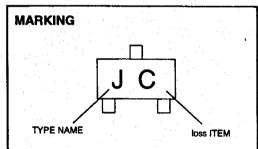
#### **FEATURE**

- Small type for mounting.
- ●High lyfsl | lyfsl = 4mS (typ)
- ●Low RDS(ON) RDS(ON)=220 Ω

### **APPLICATION**

General purpose voltage amplify, analog switch circuit for stereo, cassette deck, VCR.





### MAXIMUM RATINGS (Ta=25°C)

Symbol	Parameter	Ratings	Unit
VGDO	Gate to Drain voltage	50	V
lG	Gate Current	-10	mA
Рт	Total allowable dissipation (Ta = 25 °C)	150	mW
Tch .	Channel temperature	+125	Č
Tstg	Storage temperature	-55 to +125	°C

## **ELECTRICAL CHARACTERISTICS (Ta=25°C)**

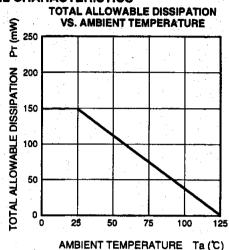
Symbol	Parameter	Test conditions		Limits		
				Тур	Max	Unit
V(BR)GDO	G to D break down voltage	IG =10 μA, Is=0	50			V
lgss	Gate leakage current	Vgs = 30V, Vos=0			1	nA
IDSS *	Drain current	Vps =-10V, Vgs=0	-1.0	-4.0	-12	mA
VGS (off)	Cut off voltage	VDS =-10V, ID= -10 μ A	0.3	1.5	6.0	V
l yfs l	Forward transfer admittance	Vps =-10V, Vgs=0, f=1kHz	1.5	4.0		mS
Ciss	Input capacitance	Vps =-10V, Vgs=0, f=1MHz		18		pF
RDS(ON)	Drain to source resistor	Vps=10mVrms(1kHz), Vgs=0, lpss=5mA		220		Ω

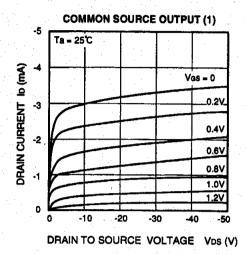
<sup>\* :</sup> It shows loss classification in right table.

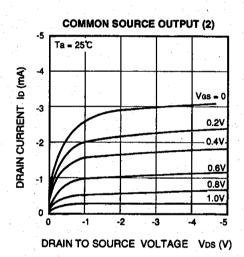
ltem	С	D	E
IDSS (mA)	1.0 to 3.0	2.5 to 6.0	5.0 to 12

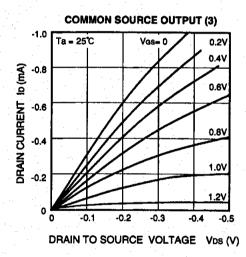
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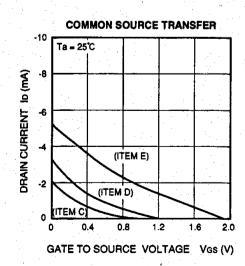


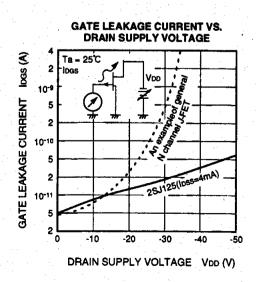




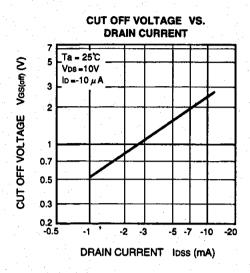


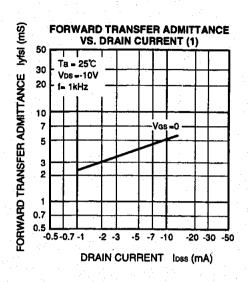


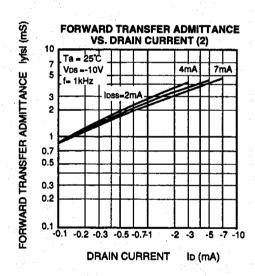


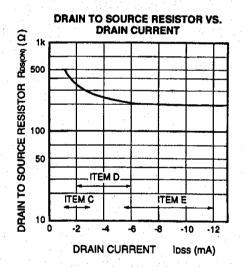


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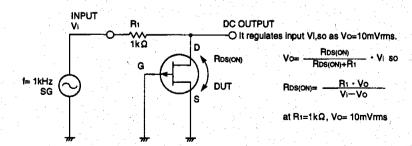








## DRAIN TO SOURCE RESISTOR RDS(ON) TEST CIRCUIT





http://www.idc-com.co.jp 6-41, TSUKUBA, ISAHAYA, NAGASAKI, 854-0065, JAPAN

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