



P-Channel 60-V (D-S) MOSFET

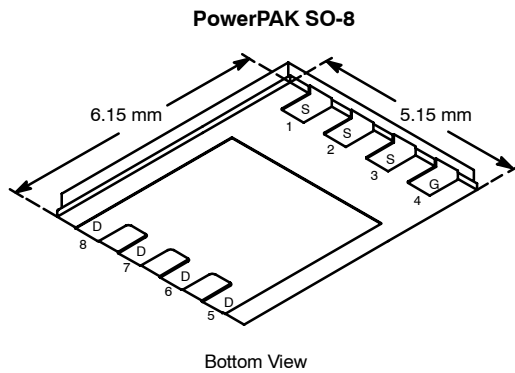
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-60	0.0145 @ $V_{GS} = -10$ V	-14.4
	0.019 @ $V_{GS} = -4.5$ V	-12.6

FEATURES

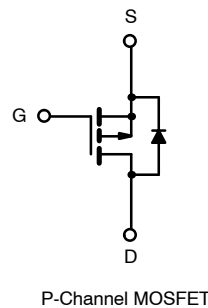
- TrenchFET® Power MOSFET
- New Low Thermal Resistance PowerPAK® Package with Low 1.07-mm Profile

APPLICATIONS

- Automotive
 - 12-V Boardnet
 - High-Side Switches
 - Motor Drives



Ordering Information: Si7461DP-T1—E3



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage	V_{DS}	-60		V	
Gate-Source Voltage	V_{GS}	± 20			
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	$T_A = 25^\circ\text{C}$	-14.4	-8.6	A
		$T_A = 70^\circ\text{C}$	-11.5	-6.9	
Pulsed Drain Current	I_{DM}	-60			
Continuous Source Current (Diode Conduction) ^a	I_S	-4.5	-1.6		
Avalanche Current	I_{AS}	50		mJ	
Single Pulse Avalanche Energy		E_{AS}	125		
Maximum Power Dissipation ^a	P_D	$T_A = 25^\circ\text{C}$	5.4	1.9	W
		$T_A = 70^\circ\text{C}$	3.4	1.2	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R_{thJA}	$t \leq 10$ sec	18	23	$^\circ\text{C/W}$
		Steady State	52	65	
Maximum Junction-to-Case (Drain)	R_{thJC}	1.0	1.3		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

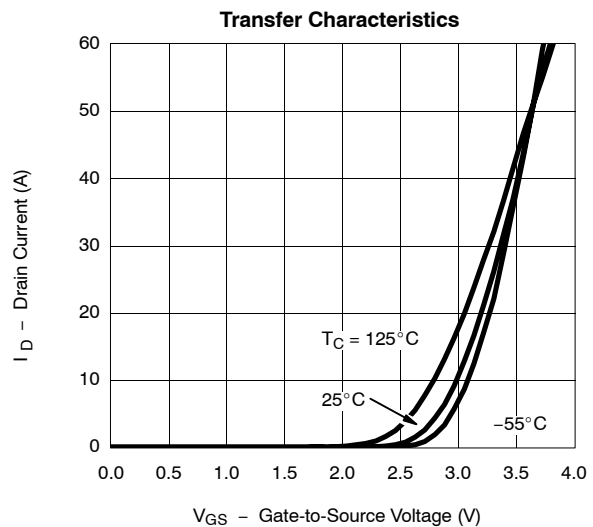
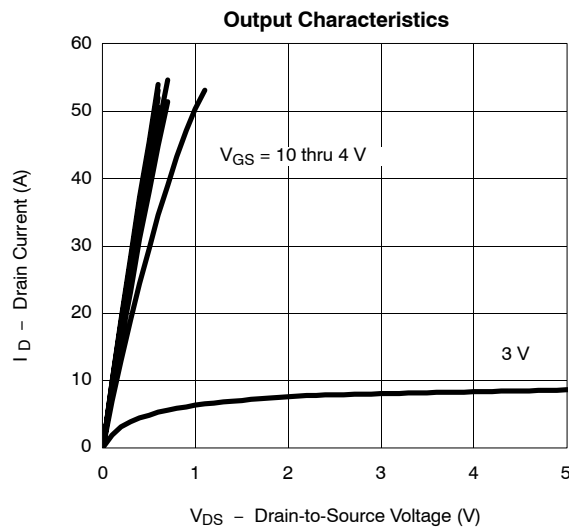
SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-1		-3	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -60 V, V _{GS} = 0 V, T _J = 70 °C			-10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≤ -5 V, V _{GS} = -10 V	-40			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = -10 V, I _D = -14.4 A		0.0115	0.0145	Ω
		V _{GS} = -4.5 V, I _D = -12.6 A		0.015	0.019	
Forward Transconductance ^a	g _{fs}	V _{DS} = -15 V, I _D = -14.4 A		31		S
Diode Forward Voltage ^a	V _{SD}	I _S = -4.5 A, V _{GS} = 0 V		-0.8	-1.2	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -30 V, V _{GS} = -10 V, I _D = -14.4 A		121	190	nC
Gate-Source Charge	Q _{gs}			20		
Gate-Drain Charge	Q _{gd}			32		
Gate-Resistance	R _g			3		Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = -30 V, R _L = 30 Ω I _D ≅ -1 A, V _{GEN} = -10 V, R _g = 6 Ω		20	30	ns
Rise Time	t _r			20	30	
Turn-Off Delay Time	t _{d(off)}			205	310	
Fall Time	t _f			90	135	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = -4.5 A, di/dt = 100 A/μs		45	70	

Notes

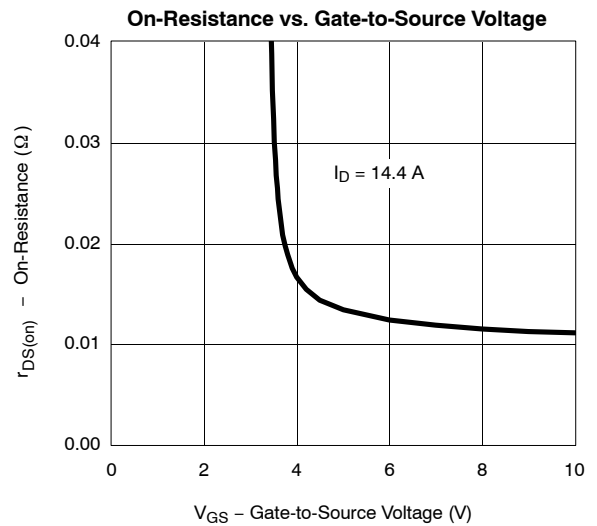
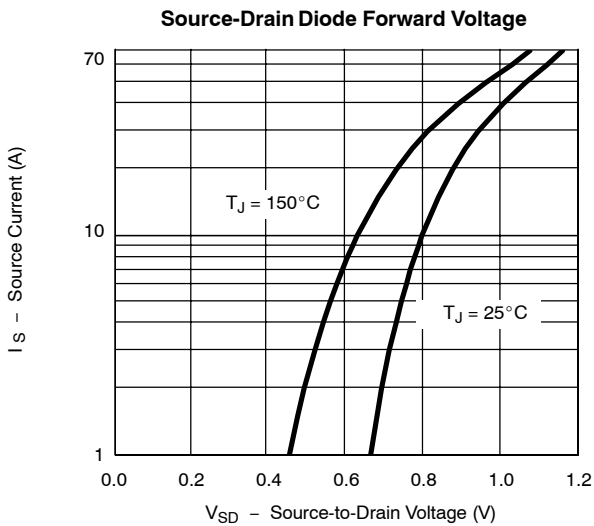
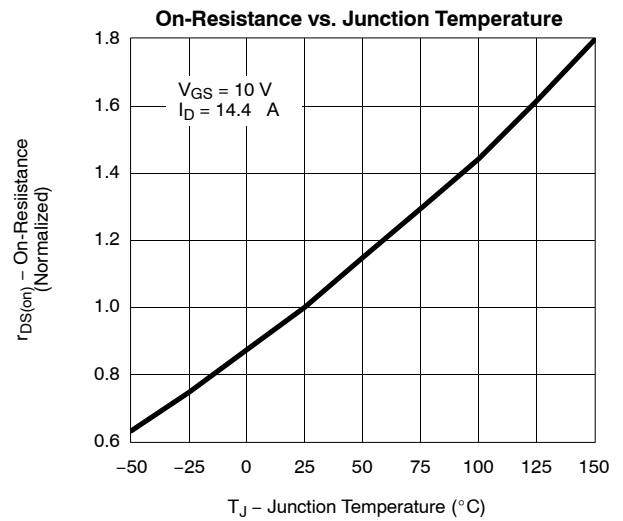
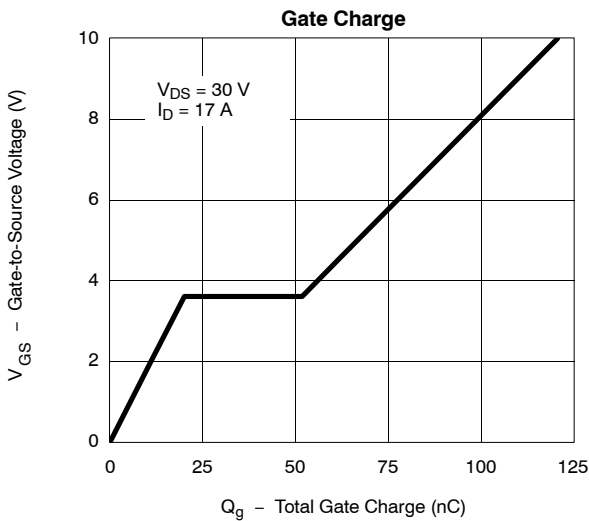
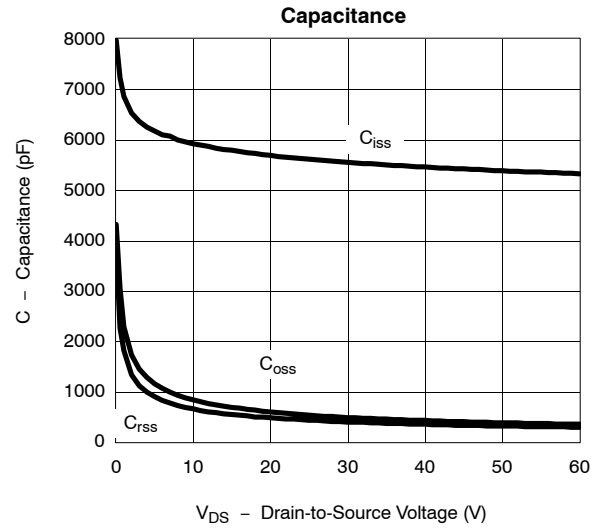
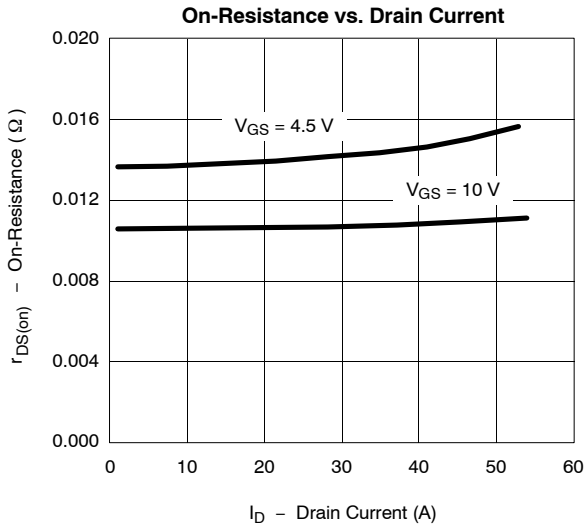
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

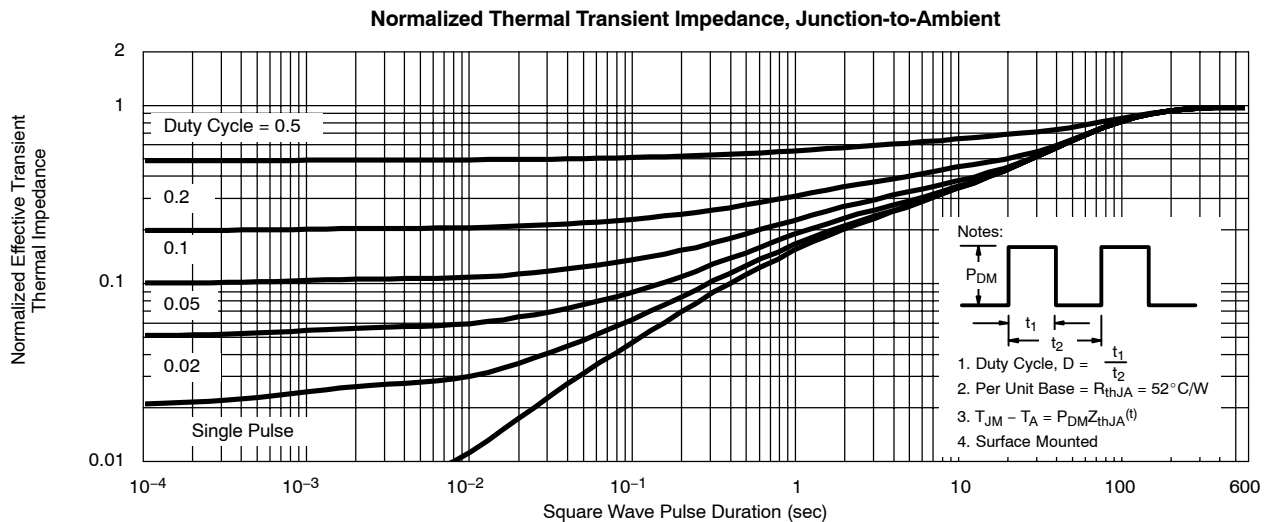
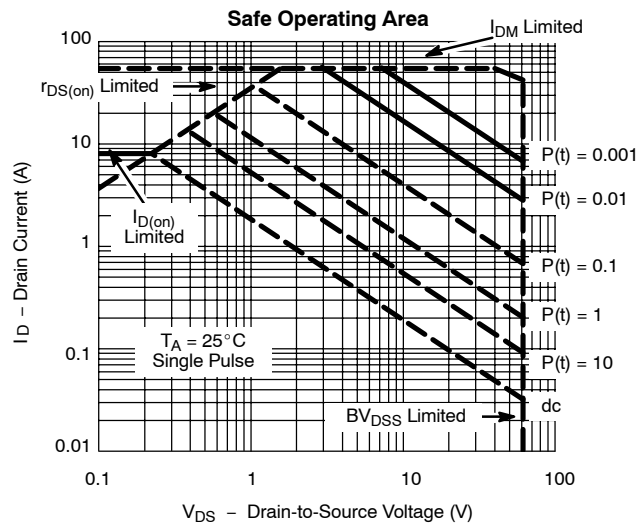
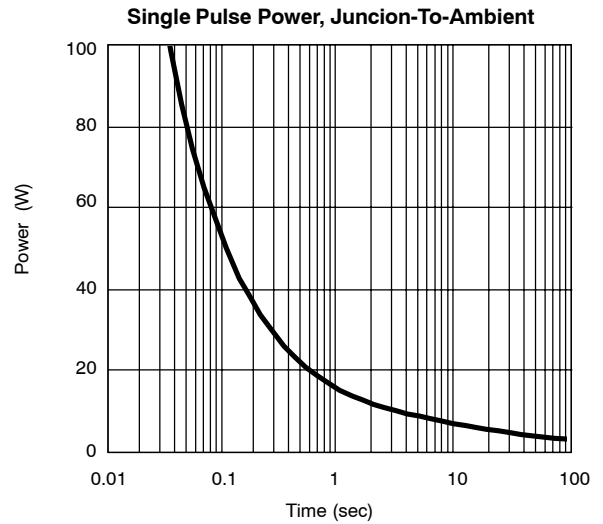
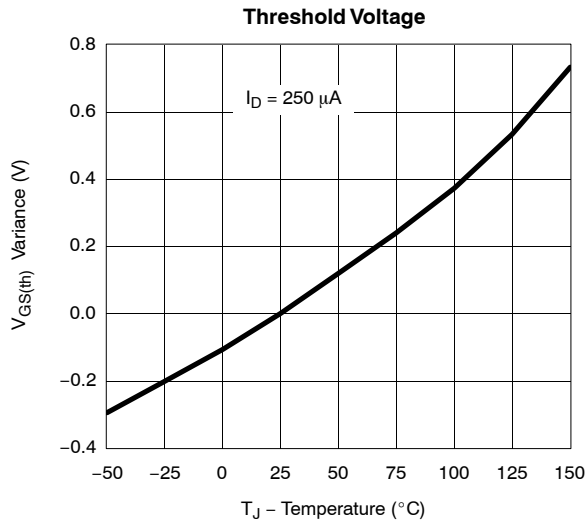




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