

HRW0302A

Silicon Schottky Barrier Diode for Rectifying

HITACHI

Rev. 5
Nov. 1994

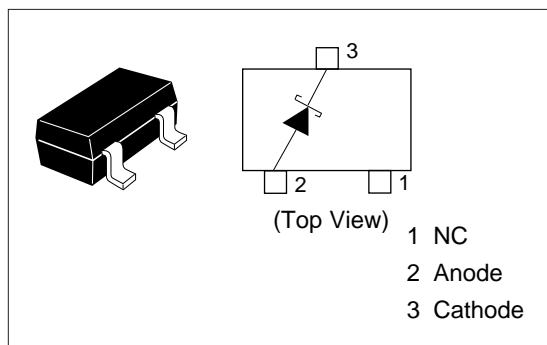
Features

- Low forward voltage drop and suitable for high efficiency rectifying.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HRW0302A	S11	MPAK

Pin Arrangement



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}^*	20	V
Average forward current	$I_o^{*,**}$	300	mA
Non-Repetitive peak forward surge current	I_{FSM}^{***}	3	A
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

* See Fig.4 & Fig.5

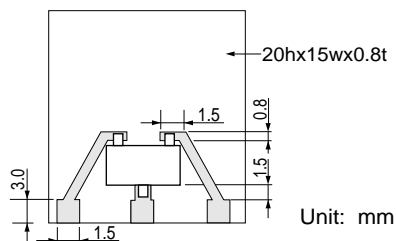
** Square waave, Duty (1/2)

*** 10msec half sine wave 1 pulse

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_R	—	—	100	μ A	$V_R = 20$ V
Forward voltage	V_F	—	—	0.4	V	$I_F = 300$ mA
Capacitance	C	—	—	100	pF	$V_R = 0$ V, f= 1MHzThermal
Thermal resistance	$R_{th(j-a)}$	—	340	—	°C/W	Polyimide substrate *

* Polyimide PCB



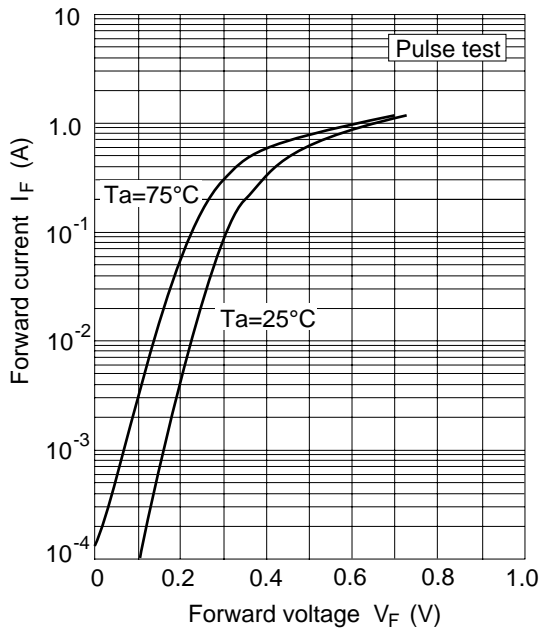


Fig.1 Forward current Vs. Forward voltage

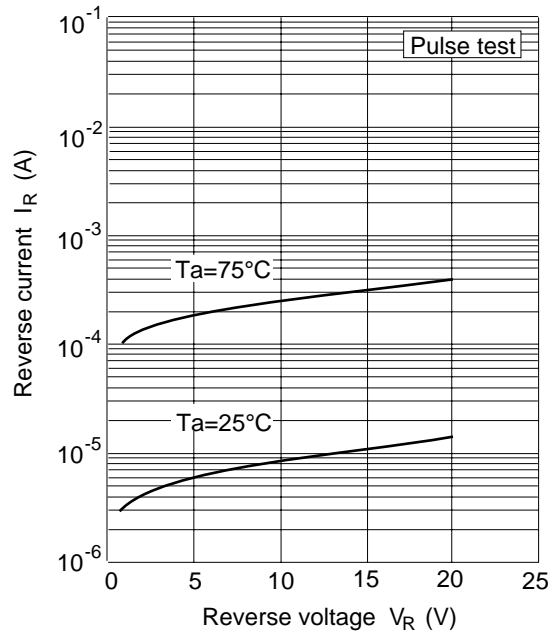


Fig.2 Reverse current Vs. Reverse voltage

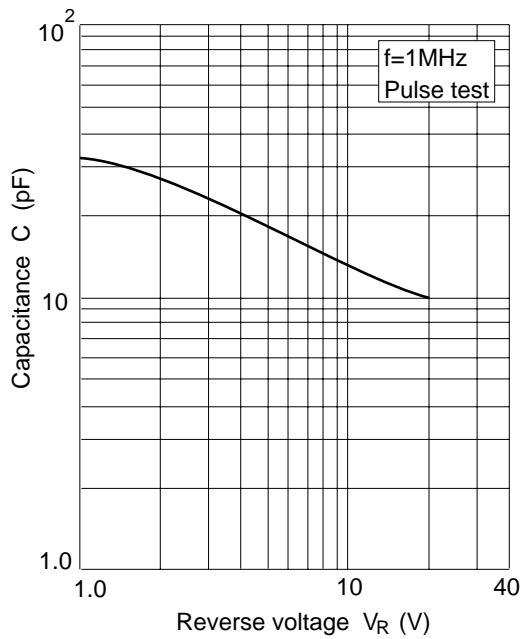


Fig.3 Capacitance Vs. Reverse voltage

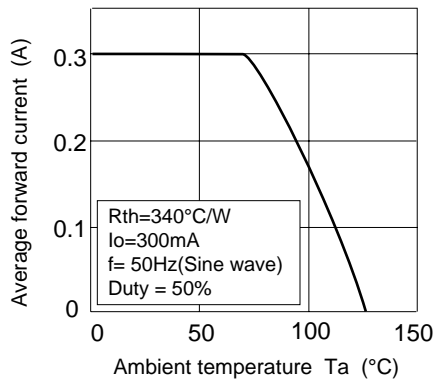


Fig.4 Average forward current Vs. Ambient temperature

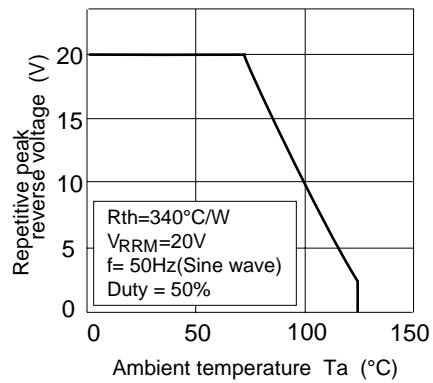
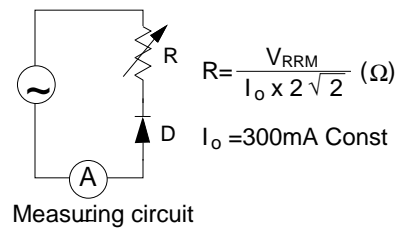
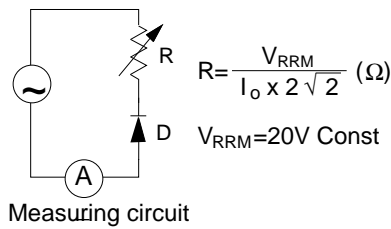
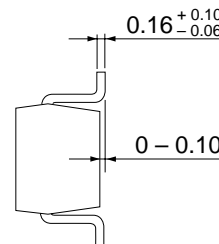
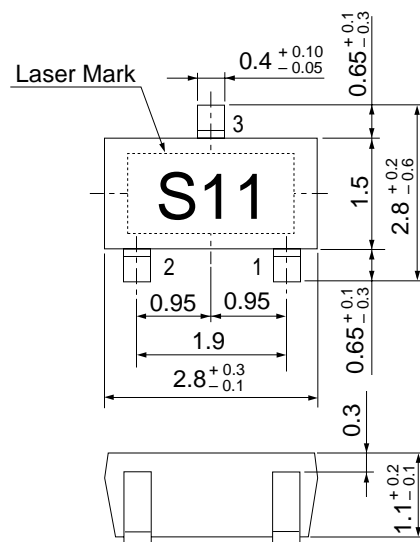


Fig.5 Repetitive peak reverse voltage Vs. Ambient temperature



Package Dimensions

Unit: mm



- 1 NC
- 2 Anode
- 3 Cathode

HITACHI Code	MPAK(1)
JEDEC Code	—
EIAJ Code	SC-59A
Weight (g)	0.011