

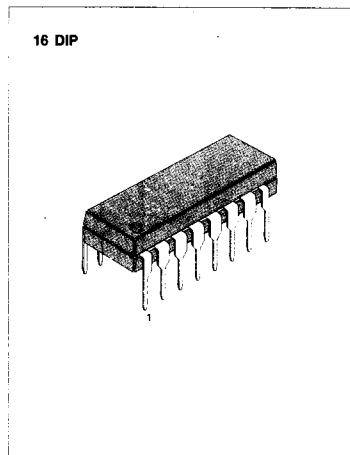
SPEECH NETWORK WITH DIALER INTERFACE

The KA8502A/B is a telephone speech network integrated circuit which includes transmit amp, receive amp, DTMF amp, voltage regulator, line equalizer, voltage comparator. It handles the voice signal, performing the 2/4 wires interface and changing the gain on both sending and receiving amplifiers to compensate for line attenuation by sensing the line length through the line current. The KA8502A/B can work in fixed gain mode.

FEATURES

- Adjusts sending and receiving attenuation length
- Regulated voltage for dialer
- Linear Interface for DTMF
- Suitable for ceramic transducers
- Mute function

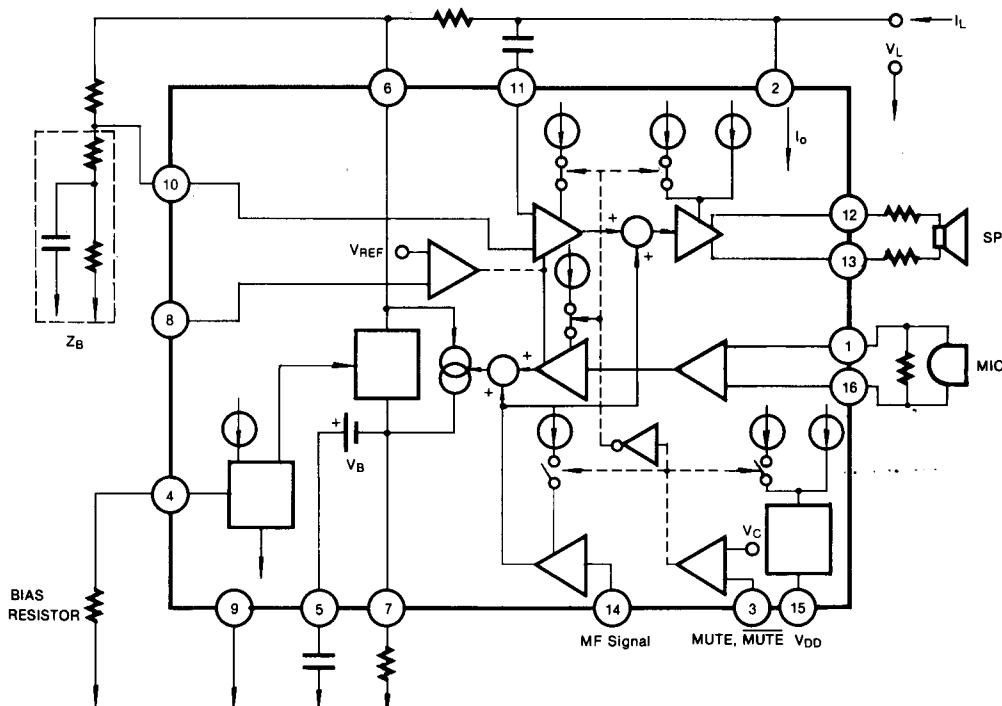
BLOCK DIAGRAM



ORDERING INFORMATION

Device	Function	Package	Operating Temperature
*KA8502AN	MUTE	16 DIP	-45 ~ +70°C
*KA8502BN	MUTE		

* Under Development



ABSOLUTE MAXIMUM RATINGS

Characteristics	Symbol	Value	Unit
Line Voltage (3msec max)	V_L	22	V
Forward Line Current	I_{LF}	150	mA
Reverse Line Current	I_{LR}	- 150	mA
Power Dissipation ($T_a = 70^\circ\text{C}$)	P_D	1	W
Operating Temperature	T_{opr}	- 45 ~ + 70	°C
Storage Temperature	T_{stg}	- 65 ~ + 150	°C

ELECTRICAL CHARACTERISTICS

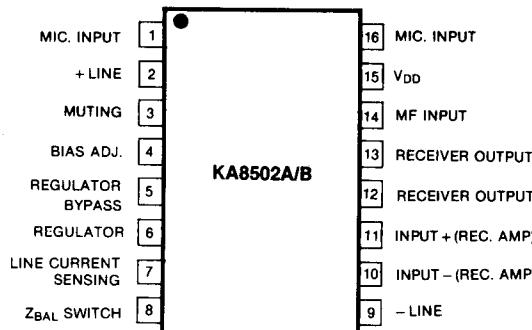
(Refer to the test circuits, S1 and S2 in(a) $V_G = 1 \sim 2\text{V}$, $I_L = 12 \sim 80\text{mA}$, $f = 200 \sim 3400\text{Hz}$, $T_a = - 20 \sim + 70^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Test Condition	Test Fig.	KA8502A/B			Unit
				Min	Typ	Max	
Line Voltage	V_L	$T_a = 25^\circ\text{C}$		$I_L = 12\text{mA}$	3.65	4.5	V
				$I_L = 20\text{mA}$		5.0	
				$I_L = 80\text{mA}$		10.0	
Common Mode Rejection Ratio	CMRR	$f = 1\text{KHz}$ $I_L = 12 \sim 80\text{mA}$	1	50			dB
Line Matching Impedance	Z_L	$V_{RI} = 0.3\text{V}$, $I_L = 12 \sim 80\text{mA}$ $f = 1\text{KHz}$	3	500	600	700	Ω
Sending Gain	G_S	$T_a = 25^\circ\text{C}$ $f = 1\text{KHz}$ $V_{MI} = 2\text{mV}$	2	44.5	45.5	46.5	dB
				48.5	49.5	50.5	
Sending Gain Flatness	G_{SF}	$V_{MI} = 2\text{mV}$ $f_{ref} = 1\text{KHz}$ $I_L = 12 \sim 80\text{mA}$	2			± 1	dB
Sending Distortion	THDs	$f = 1\text{KHz}$ $I_L = 16$	2	$V_{SO} = 775\text{mV}$		2	%
				$V_{SO} = 900\text{mV}$		10	
Sending Noise	N_s	$V_{MI} = 0\text{V}$, $V_G = 1\text{V}$	2		- 71	- 69	dB _{mp}
Side Tone	ST	$T_a = 25^\circ\text{C}$, $f = 1\text{KHz}$ $S_1 = b$	2			36	dB
Micphone Input Impedance	Z_{IM}	$V_{MI} = 2\text{mV}$		40			KΩ
Receiving Gain	G_R	$T_a = 25^\circ\text{C}$ $V_{RI} = 0.3\text{V}$, $f = 1\text{KHz}$	3	- 5		- 3	dB
				- 0.5		1.5	

ELECTRICAL CHARACTERISTICS (Continued)

Characteristic	Symbol	Test Condition	Test Fig.	KA8502A/B			Unit
				Min	Typ	Max	
Receiving Gain Flatness	G _{RF}	V _{RI} = 0.3V, f _{ref} = 1KHz I _L = 12 ~ 80mA	3			± 1	dB
Receiving Distortion	THD _R	f = 1KHz	3			2	%
						5	
Receiving Noise	N _R	V _{RI} = 0V, I _L = 12 ~ 80mA	3		100	200	μV
Receiver Output Impedance		V _{RO} = 50mV,			30		Ω
MF Supply Voltage	V _{DD}	I _L = 12 ~ 80mA		2.4	2.5	2.7	V
MF Supply Current	I _{DD}	I _L = 12 ~ 80mA KA8502A; S2 = b KA8502B; S2 = a		0.5			mA
				2			
MF Amplifier Gain	G _{MF}	I _L = 12 ~ 80mA f _{MF} = 1KHz V _{MF} = 80mV	4	15		17	dB
DC Input Voltage Level (pin 14)	V _{IMF}	V _{MF} = 80mV			0.3V _{DD}		V
Input Impedance (pin 14)	Z _{MF}	V _{MF} = 80mV		60			KΩ
Distortion	THD _{MF}	V _{MF} = 110mV I _L = 12 ~ 80mA	4			2	%
Starting Delay Time	t _d	I _L = 12 ~ 80mA				5	mS
Muting Threshold Voltage (pin 3)	V _{TH}				1		V
Muting Current	I _M	I _L = 12 ~ 80mA I _L = 12 ~ 80mA KA8502A; S2 = b KA8502B; S2 = a			1.6		
						- 10	μA
Input Current (pin 8)	I ₈					+ 10	
						- 10	μA

CONNECTION DIAGRAM



TEST CIRCUIT

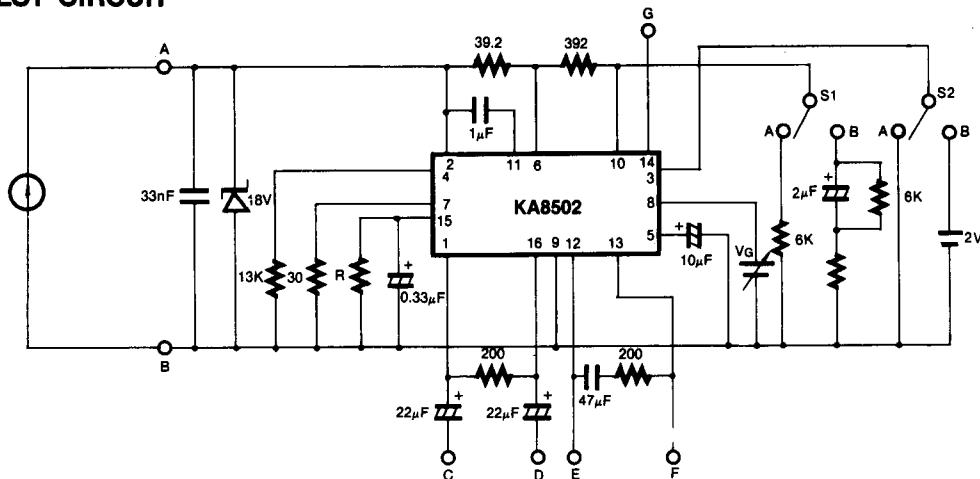


Fig. 1

Fig. 2

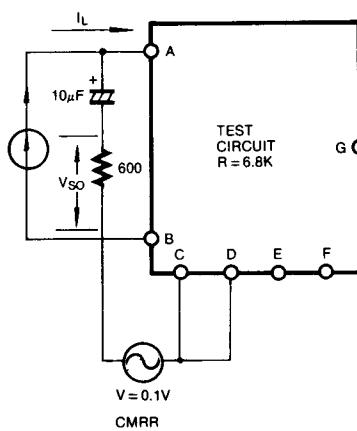


Fig. 3

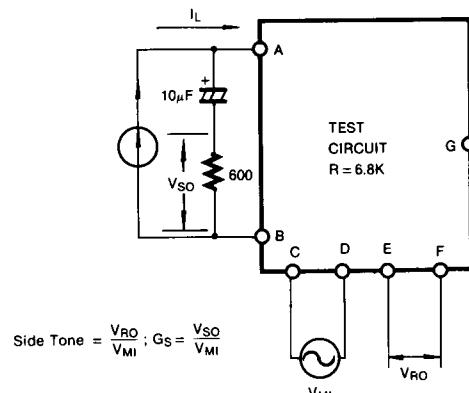


fig. 4

