

PHEMT GaAs IC High Power SP4T Switch 0.1–2.5 GHz

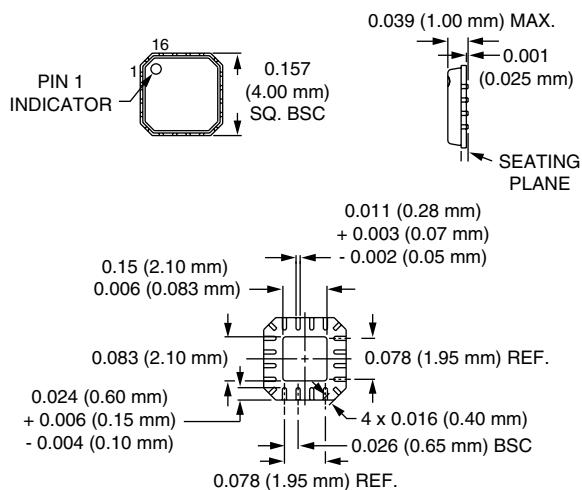


AS221-306

Features

- 4 Symmetric RF Paths
- Positive Voltage Control @ 2.6 V
- High IP3
- Excellent Harmonic Performance
- Handles GSM Power Levels
- Available in MLF-16 (4 x 4 mm) Package

QFN-16 (4 x 4 mm)



Description

The AS221-306 is a reflective SP4T switch. It is an ideal switch for higher power applications. It can be used for GSM dual-band handset applications where both low loss, low current and small size are critical parameters.

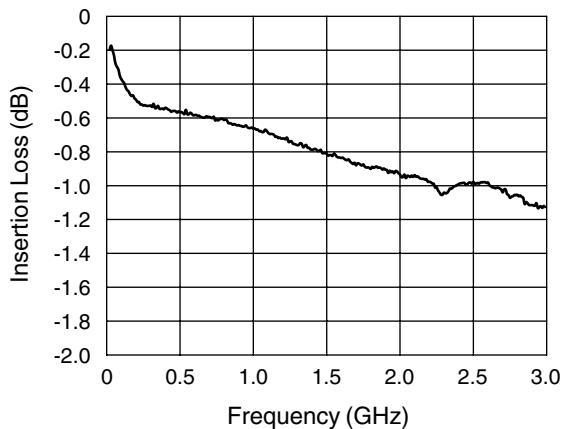
Electrical Specifications at 25°C (0, +2.6 V)

| Parameter | | Frequency | Min. | Typ. | Max. | Unit |
|----------------|---|--|----------------------|--------------------------|--------------------------|----------------------|
| Insertion Loss | Ant-J ₁ , J ₂ , J ₃ , J ₄ | 0.1–0.5 GHz 0.5–1.0 GHz 1.0–2.0 GHz 2.0–2.5 GHz | | 0.6 0.7 0.9 1.1 | 0.7 0.8 1.1 1.2 | dB dB dB dB |
| Isolation | Ant-J ₁ , J ₂ , J ₃ , J ₄ | 0.1–0.5 GHz 0.5–1.0 GHz 1.0–2.0 GHz 2.0–2.5 GHz | 30 25 19 18 | 34 29 23 22 | | dB dB dB dB |
| VSWR | | 0.1–1.0 GHz 1.0–2.5 GHz | | 1.2:1 1.3:1 | | |

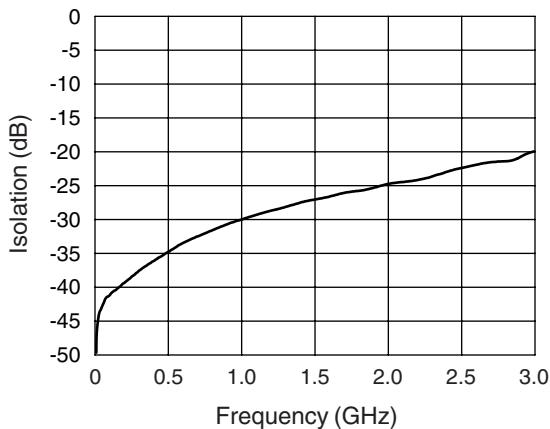
Operating Characteristics at 25°C (0, +2.6 V)

| Parameter | Condition | Frequency | Min. | Typ. | Max. | Unit |
|---------------------------|--|-----------|------|-----------------|------|----------------|
| Switching Characteristics | Rise, Fall (10/90% or 90/10% RF) On, Off (50% CTL to 90/10% RF) Video Feedthru | | | 50 100 50 | | ns ns mV |
| IP3 | 13 dBm/Tone | | | +55 | | dBm |
| 2nd and 3rd Harmonics | 34 dBm Input 900 MHz | | | +65 | | dBc |
| Control Voltages | V _{Low} = 0 V _{High} = 2.6 V @ 200 μA | | | | | |

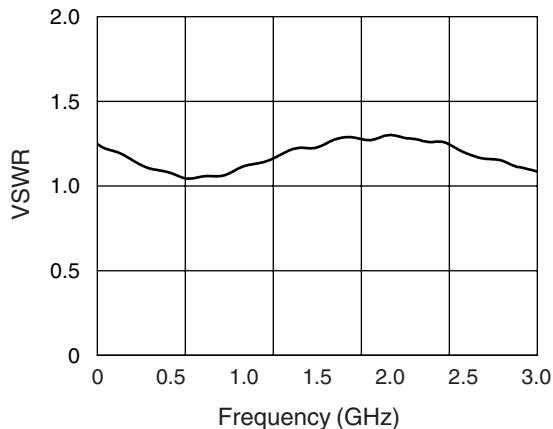
Typical Performance Data



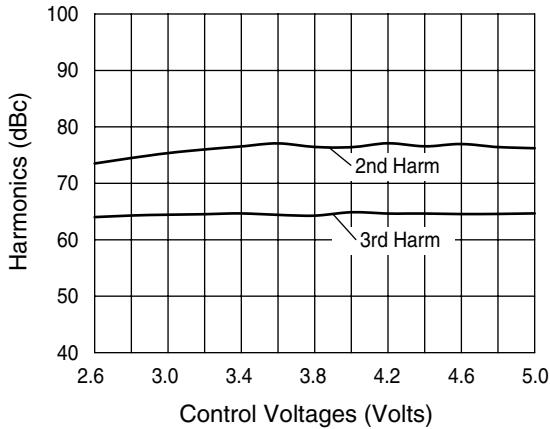
Typical Insertion Loss vs. Frequency



Typical Isolation vs. Frequency



Typical VSWR vs. Frequency



Typical Harmonics vs. Control Voltages

Absolute Maximum Ratings

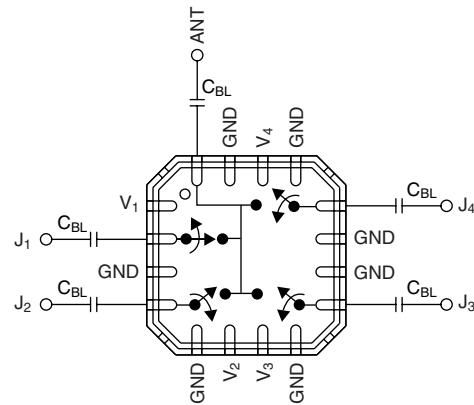
| Characteristic | Value |
|-----------------------|---------------------------------|
| RF Input Power | 4 W > 0.5 GHz 0/+6 V Control |
| Control Voltage | +6 V |
| Operating Temperature | -40°C to +85°C |
| Storage Temperature | -65°C to +150°C |
| θ_{JC} | 25°C/W |

Truth Table

| V_1 | V_2 | V_3 | V_4 | Ant-J ₁ | Ant-J ₂ | Ant-J ₃ | Ant-J ₄ |
|------------|------------|------------|------------|--------------------|--------------------|--------------------|--------------------|
| V_{High} | V_{Low} | V_{Low} | V_{Low} | Ins. Loss | Isolation | Isolation | Isolation |
| V_{Low} | V_{High} | V_{Low} | V_{Low} | Isolation | Ins. Loss | Isolation | Isolation |
| V_{Low} | V_{Low} | V_{High} | V_{Low} | Isolation | Isolation | Ins. Loss | Isolation |
| V_{Low} | V_{Low} | V_{Low} | V_{High} | Isolation | Isolation | Isolation | Ins. Loss |

$V_{Low} = 0$
 $V_{High} = 2.6$ V.

Pin Out



DC blocking capacitors (C_{BL}) must be supplied externally.
 $C_{BL} = 47$ pF for operating frequency >500 MHz.