

## Optical Communication Receiver/Amplifier

The HA16853MP is a receiving amplifier-IC for digital data receiver via optical fiber. The circuit includes a pre-main Amplifier, a comparator, ATC and TTL output stage, in a mini-square package.

### Features

- Single 5 V Supply
- DC to 10 Mbits/s NRZ Data rate
- TTL Compatibility
- Internal Automatic Threshold Control
- 18-pin Surface mount package

### Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Ratings	Unit	Notes
Supply Voltage	V <sub>CC</sub>	0 to 7	V	V <sub>CC1</sub> , V <sub>CC2</sub> , V <sub>CC3</sub>
Input Current	I <sub>INP</sub>	0 to 1	mA	Pin
Output Current	I <sub>OUTP</sub>	-1 to 1	mA	P <sub>OUT</sub>
	I <sub>SIG</sub>	0 to 2	mA	SIG
Power Dissipation	P <sub>D</sub>	500	mW	
Operating Temperature	T <sub>OPR</sub>	-20 to +75	°C	
Storage Temperature	T <sub>STG</sub>	-55 to +125	°C	

### Electrical Characteristics

#### DC characteristics ( $T_a = 25^\circ\text{C}$ , $V_{CC} = 5 \text{ V}$ )

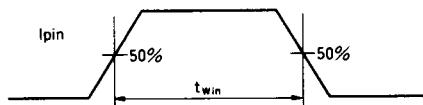
Item	Symbol	Test conditions	Min	Typ	Max	Unit	Notes
Supply Voltage	V <sub>CC</sub>		4.75	5.00	5.25	V	
Supply Current	I <sub>CC</sub>	$V_{CC} = 5 \text{ V}$ , $I_{IN} = 0 \mu\text{A}$	15	19	27	mA	
Offset Voltage	V <sub>OFF</sub>	$V_{CC} = 5 \text{ V}$ , $I_{IN} = 0 \mu\text{A}$ (refi.-C <sub>in</sub> )	19	40	67	mV	1
Output Voltage	V <sub>OL</sub>	$I_{IN} = 0 \mu\text{A}$	—	—	0.4	V	
		$V_{CC} = 4.75 \text{ V}$	$I_{OL} = 2 \text{ mA}$				
	V <sub>OH</sub>		$I_{IN} = 20 \mu\text{A}$	2.7	—	—	V
			$I_{OH} = -400 \mu\text{A}$				

Note: 1. Voltage difference from C<sub>in</sub> to ref.

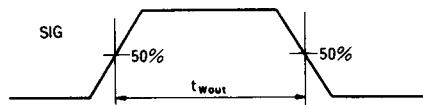
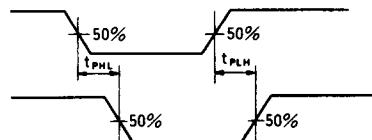
#### AC Characteristics ( $T_a = 25^\circ\text{C}$ , $f = 5 \text{ MHz}$ , $V_{CC} = 5 \text{ V}$ )

Item	Symbol	Test conditions	Min	Typ	Max	Unit	Notes
Propagation	t <sub>PLH</sub>	$V_{CC} = 5 \text{ V}$ , I <sub>IPIN</sub> = 2 $\mu\text{A}$	—	—	80	ns	
Delay-time	t <sub>PHL</sub>		—	—	80	ns	
Output Pulse Width fluctuation	Δ t <sub>W</sub>	$V_{CC} = 5 \text{ V}$ , I <sub>IPIN</sub> = 2 $\mu\text{A}$	-10	—	+10	%	

Note: I<sub>IPIN</sub> is input Current of Pin terminal.

**Output Pulse Width Fluctuation**

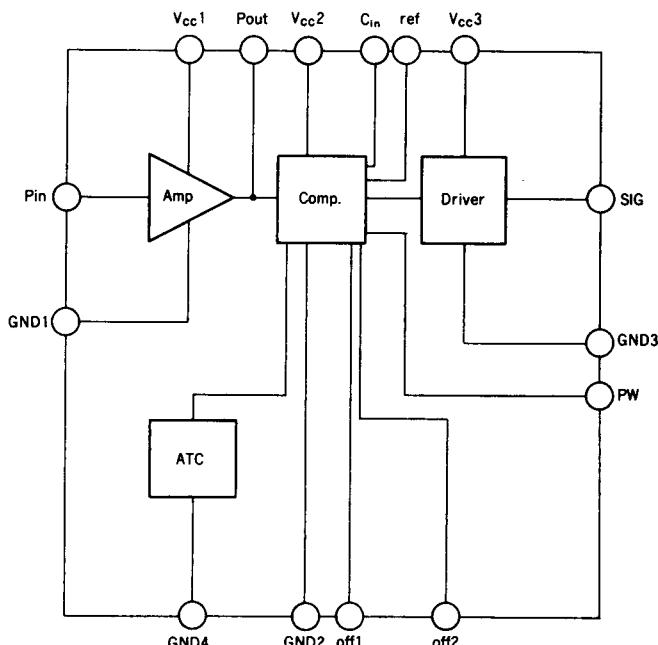
$$\Delta t_w = \left( \frac{t_{wout}}{t_{win}} - 1 \right) \times 100$$

**Propagation Delay Time**

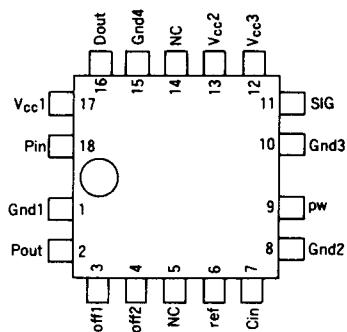
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## Block Diagram



## Pin Arrangement



(Top View)

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**Pin Description**

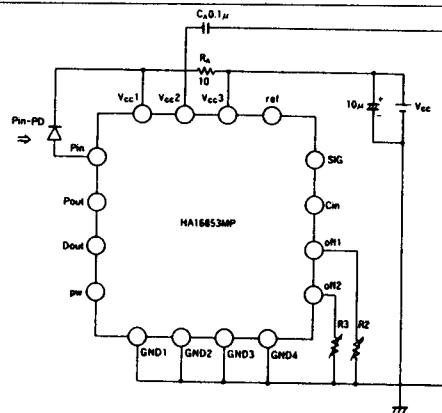
Pin No	Pin Name	Function Description
1	Gnd1	Ground pin (Internal Receiver Amp. Circuit Ground)
2	Pout	Receiver Amp. output
3	off1	Resistor run between this terminal and ground for (ref) terminal output level adjustment
4	off2	Resistor run between this terminal and ground for (Cin) comparator input level adjustment
5	N.C	No Connection pin
6	ref	Automatic Threshold Control Circuit Output
7	Cin	Comparator input
8	Gnd2	Ground pin (Internal Comparator, ATC Circuit Ground)
9	pw	Output adjustment (Connect to ground through the resistor)
10	Gnd3	Ground Pin (TTL Output Circuit Ground)
11	SIG	TTL Digital output
12	Vcc3	5 V Voltage source input for TTL output circuit
13	Vcc2	5 V Voltage source input for Comparator and ATC
14	N.C	No Connection pin
15	Gnd4	Connected to ground
16	Dout	Reference Amplifier Output
17	Vcc1	5V Voltage Source input for Receiver Amp.
18	Pin	Receiver Amp. input

**Packaging Information**

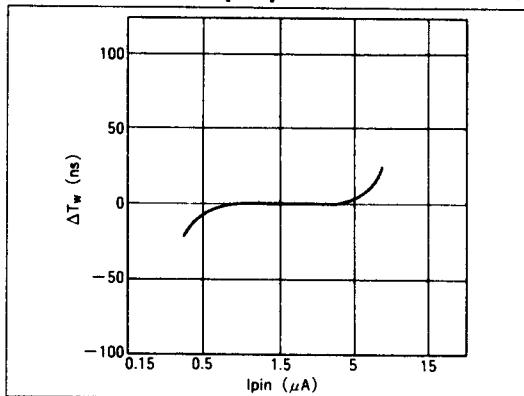
Part No.	Package
HA16853MP	MP-18

# HA16853MP

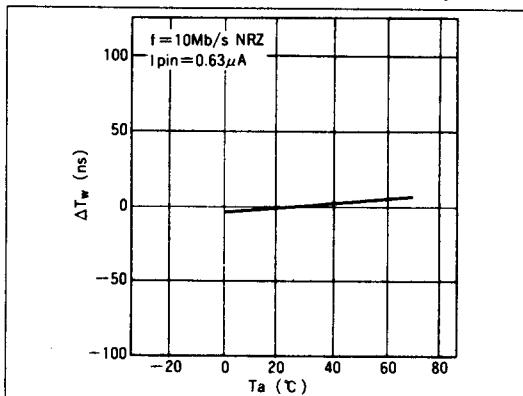
## Application Circuit



**Input Current vs. Output pulse Width Fluctuation**



**Output pulse Width Fluctuation VS. Temperature**



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