TOSHIBA TD62503,504PA

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62503PA, TD62504PA

7CH SINGLE DRIVER: COMMON EMITTER

The TD62503PA and TD62504PA are comprised of seven or five NPN transistor arrays.

Applications include relay, hammer, lamp and display (LED) drivers.

FEATURES

- Output current (single output) 200mA/ch (Max.)
- High sustaining voltage output 35V (Min.)
- Low saturation voltage V_{CE} (sat) = 0.8V @I_{OUT} = 150mA
- Inputs compatible with various types of logic.

: $R_{IN} = 2.7k\Omega$ TTL, 5V CMOS TD62503PA

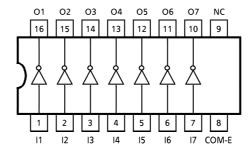
: $R_{IN} = 10.5k\Omega$ PMOS, CMOS TD62504PA

Package type-PA: DIP-16 pin

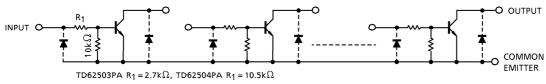
DIP16-P-300-2.54A

Weight: 1.11g (Typ.)

PIN CONNECTION (TOP VIEW)



SCHEMATICS (EACH DRIVER)



(Note) The input and output parasitic diodes cannot be used as clamp diodes.

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MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Emitter Voltage	V _{CEO}	35	V
Collector-Base Voltage	V _{CBO}	50	٧
Collector Current	ΙC	200	mA / ch
Input Voltage	VIN	-0.5~30	V
Power Dissipation	P _D (Note)	1.0	W
Operating Temperature	T _{opr}	-40∼85	°C
Storage Temperature	T _{stg}	- 55∼150	°C

(Note) Delated above 25°C in the proportion of $8.0 \, \text{mW} \, / \, ^{\circ}\text{C}.$

RECOMMENDED OPERATING CONDITIONS (Ta = $-40 \sim 85$ °C)

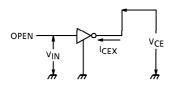
CHARACT	ERISTIC	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Collector-Emitte	er Voltage	VCEO	_	0	_	35	V
Collector-Base	Voltage	V _{CBO}	_	0	_	50	V
Collector Curre	nt	ΙC	_	0	_	150	mA / ch
Input Voltage	TD62503PA TD62504PA	V _{IN}	_	0	_	25	٧
Power Dissipation		PD	_	_	_	0.360	W

ELECTRICAL CHARACTERISTICS (Ta = 25°C unless otherwise noted)

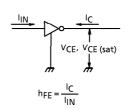
CHARACT	ERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Leakag	e Current	ICEX	1	$V_{CE} = 25V, V_{IN} = 0$	_	_	10	μ A
Collector-Emitter Saturation		V65 (1)	2	$I_{IN} = 1mA$, $I_C = 10mA$		1	0.2	V
Voltage		VCE (sat)	-	$I_{IN} = 3mA, I_C = 150mA$		_	0.8	
DC Current Tra	nsfer Ratio	hFE	2	$V_{CE} = 10V, I_{C} = 10mA$	50	_	_	_
Input Voltage	TD62503PA	V (01)	2	3 I _{IN} = 1mA, I _C = 10mA	2.4	3.4	4.2	V
(Output On)	TD62504PA	VIN (ON)			7.5	11.5	15	, v
Input Voltage	TD62505PA	V (0 ==)	_	_	0.6	0.8	1.0	V
(Output Off)	TD62506PA	VIN (OFF)			1.1	1.6	1.9	V
Turn-On Delay		^t ON	4	V_{OUT} = 35V, R_L = 3.3k Ω C_L = 15pF	_	50		ns
Turn-Off Delay		^t OFF		C _L = 15pF	_	200	_	113

TEST CIRCUIT

1. ICEX

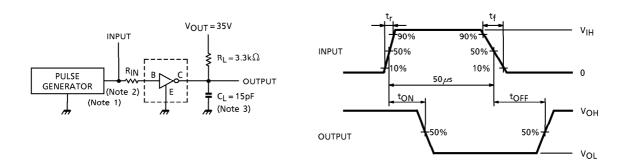


2. h_{FE}, V_{CE} (sat)



3. V_{IN} (ON)

4. ton, toff



(Note 1) Pulse Width 50 μ s, Duty Cycle 10% Output Impedance 50 Ω , $t_r \le 5$ ns, $t_f \le 10$ ns

(Note 2) See below.

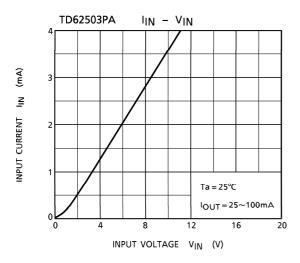
INPUT CONDITION

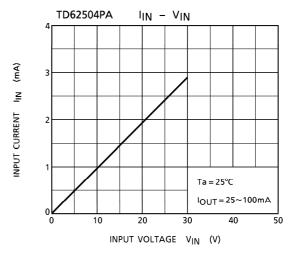
TYPE NUMBER	R _{IN}	V _{IH}
TD62503PA	Ω 0	3V
TD62504PA	Ω 0	10V

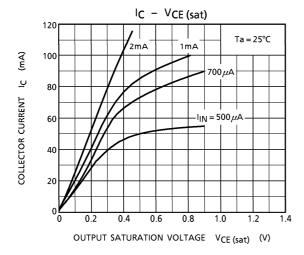
(Note 3) C_L includes probe and jig capacitance.

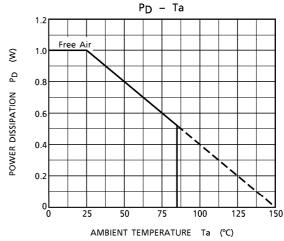
PRECAUTIONS for USING

Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



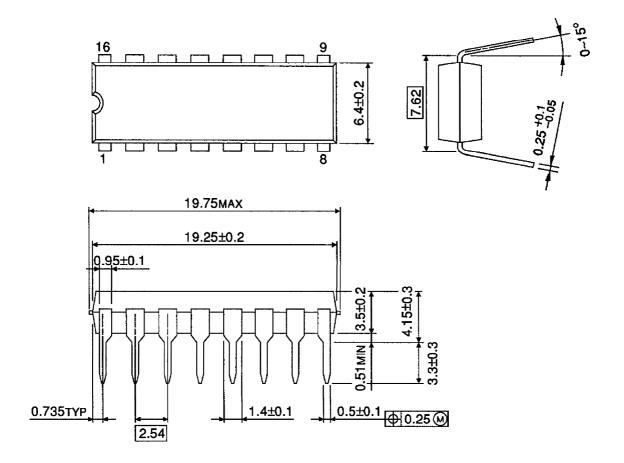






OUTLINE DRAWING DIP16-P-300-2.54A

Unit: mm



Weight: 1.11g (Typ.)