

Features

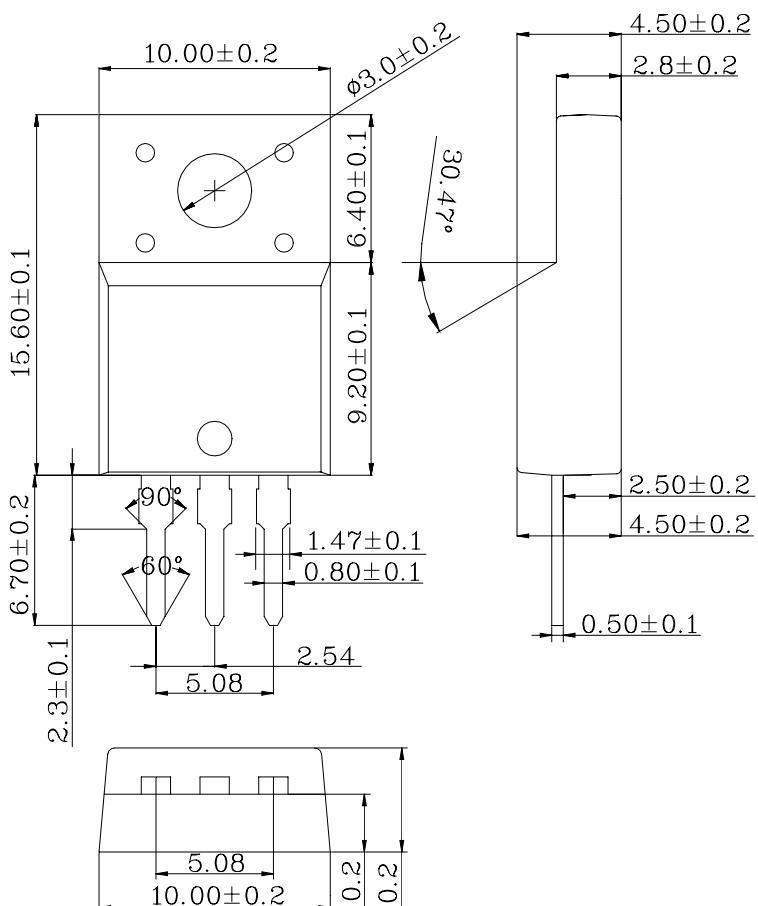
- Avalanche rugged technology.
 - Low input capacitance.
 - Improved gate charge.
 - Low leakage current : 10uA (Max.) @ $V_{DS}=500V$.
 - Low $R_{DS(ON)}$: 1.17Ω (Typ.)

Ordering Information

Type NO.	Marking	Package Code
STK830FC	STK830	TO-220F-3SL

Outline Dimensions

unit : mm



PIN Connections

1. Gate
 2. Drain
 3. Source

Absolute maximum ratings

Characteristic	Symbol	Rating	Unit
Drain-Source voltage	V_{DSS}	500	V
Gate-Source voltage	V_{GS}	± 30	V
Continuous Drain current ($T_c=25^\circ\text{C}$)	I_D	4.5*	A
Continuous Drain current ($T_c=100^\circ\text{C}$)	I_D	2.9*	A
Drain Current-Pulsed ①	I_{DM}	18	A
Power Dissipation ($T_c=25^\circ\text{C}$)	P_D	38	W
Linear Derating Factor		0.3	W/ $^\circ\text{C}$
Single Pulsed Avalanche Energy ②	E_{AS}	270	mJ
Avalanche current ①	I_{AR}	4.5	A
Repetitive Avalanche Energy ①	E_{AR}	7.3	mJ
Peak Diode Recovery dv/dt	dv/dt	5.5	V/ns
Operating Junction and Storage temperature range	T_J, T_{stg}	-55~150	$^\circ\text{C}$
Maximum lead temp. for soldering Purpose, 1/8" from case for 5-seconds	T_L	300	$^\circ\text{C}$

* Limited by Maximum junction Temperature

Thermal Resistance

Characteristic	Symbol	Typ.	Max	Units
Junction-to-Case	$R_{\theta JC}$		3.31	$^\circ\text{C}/\text{W}$
Case-to-Sink	$R_{\theta CS}$	0.5		
Junction-to-Ambient	$R_{\theta JA}$		62.5	

Electrical Characteristics (T_c=25°C unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-Source breakdown voltage	BV _{DSS}	I _D =250μA, V _{GS} =0	500			V
Gate-Threshold voltage	V _{GS(th)}	I _D =250μA, V _{DS} =5V	2.0		4.0	V
Drain-source leakage current	I _{DSS}	V _{DS} =500V			10	μA
Gate-source leakage	I _{GSS}	V _{DS} =0V, V _{GS} =±30V			±100	nA
Drain-Source on-resistance ⁽⁴⁾	R _{DS(ON)}	V _{GS} =10V, I _D =2.25A			1.5	Ω
Forward transconductance ⁽⁴⁾	g _{fs}	V _{DS} =50V, I _D =2.25A		3.87		S
Input capacitance	C _{iss}	V _{GS} =0V, V _{DS} =25V, f=1MHz		760	900	pF
Output capacitance	C _{oss}			85	100	
Reverse transfer capacitance	C _{rss}			15	22	
Turn-on delay time	t _{d(on)}	V _{DD} =250V, I _D =4.5A R _G =12Ω		15	40	ns
Rise time	t _r			16	40	
Turn-off delay time	t _{d(off)}			66	140	
Fall time	t _f			22	55	
Total gate charge	Q _g	V _{DS} =400V, V _{GS} =10V, I _D =4.5A		33	43	nC
Gate-source charge	Q _{gs}			4.4		
Gate-drain("Miller")charge	Q _{gd}			16.6		

Source-Drain Diode Ratings and Characteristics

Characteristic	Symbol	Test Condition	Min	Typ	Max	Units
Continuous source current	I _S	Integral reverse pn-diode in the MOSFET			4.5	A
Pulsed-source current ⁽¹⁾	I _{SM}				18	
Diode forward voltage ⁽⁴⁾	V _{SD}	T _J =25°C, V _{GS} =0V, I _S =4.5A			1.4	V
Reverse recovery time	t _{rr}	T _J =25°C, I _F =4.5A di _F /dt=100A/us		285		ns
Reverse recovery charge	Q _{rr}			2.0		uC

Note :

- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ② L=30mH, I_{AS}=4.5A, V_{DD}=50V, R_G=27Ω , starting T_J=25°C
- ③ I_{SD} ≤ 4.5A, di/dt≤ 130A/us, V_{DD}≤ BV_{DSS}, starting T_J=25°C
- ④ Pulse Test : Pulse Width=250us, Duty cycle≤ 2%
- ⑤ Essentially independent of operating temperature

Electrical Characteristic Curves

Fig. 1 I_D - V_{DS}

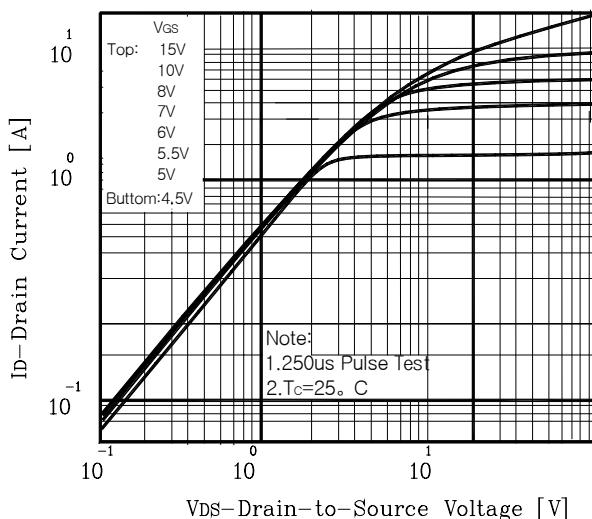


Fig. 2 I_D - V_{GS}

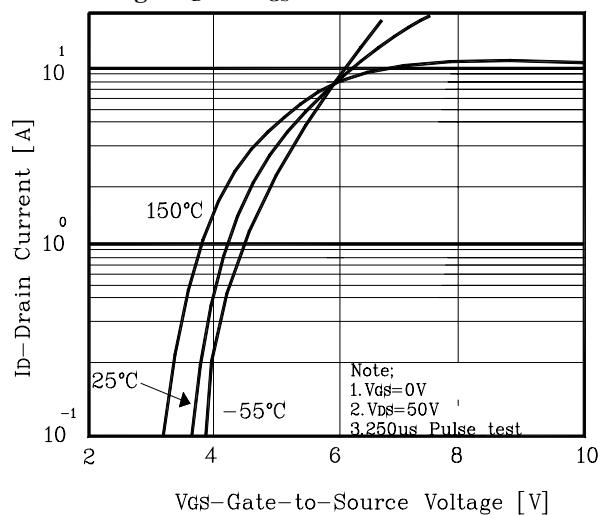


Fig. 3 $R_{DS(on)}$ - I_D

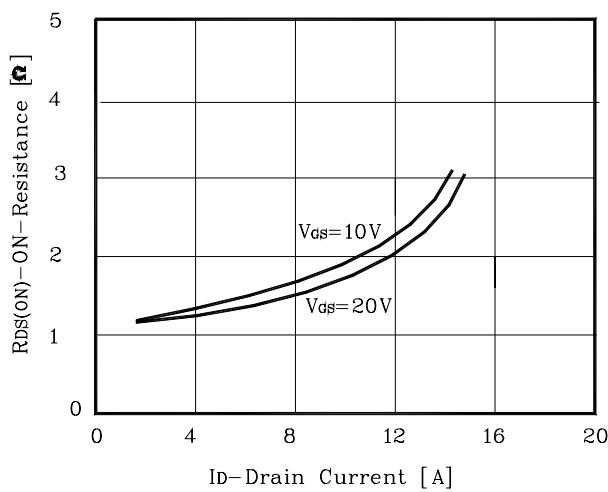


Fig. 4 I_{DR} - V_{SD}

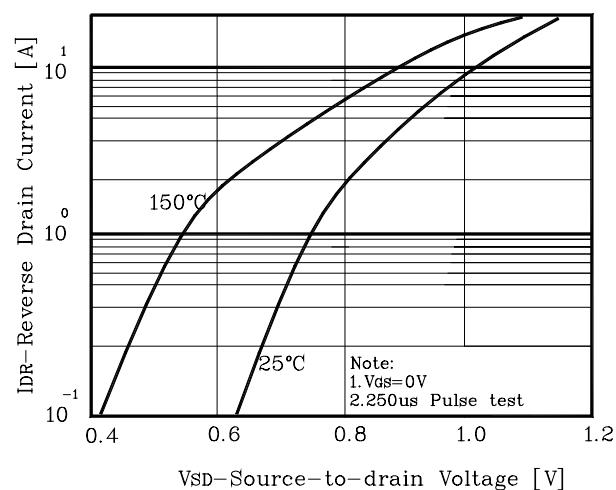


Fig. 5 Capacitance - V_{DS}

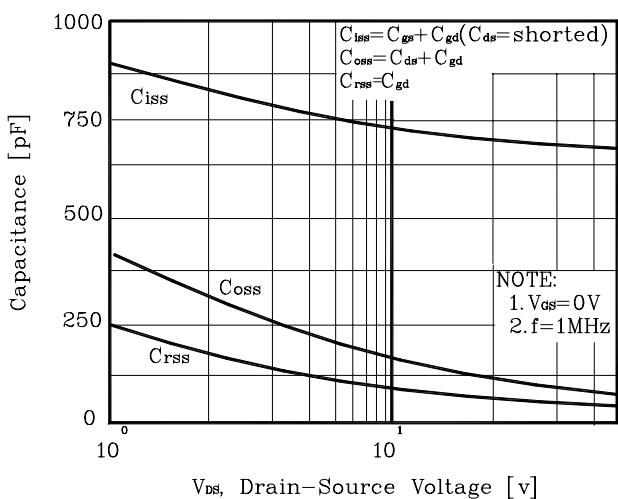


Fig. 6 V_{GS} - Q_G

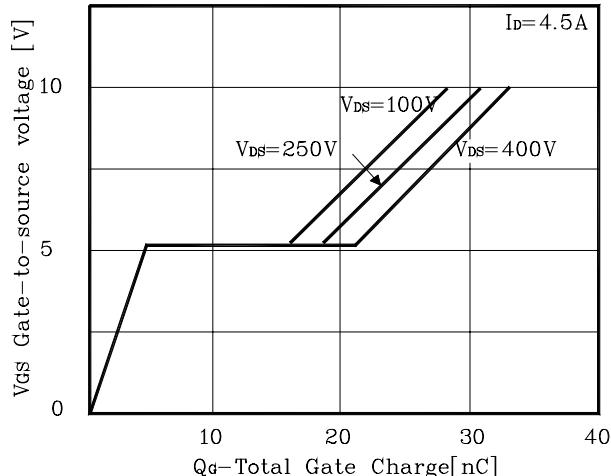


Fig. 7 BV_{DSS} - T_J

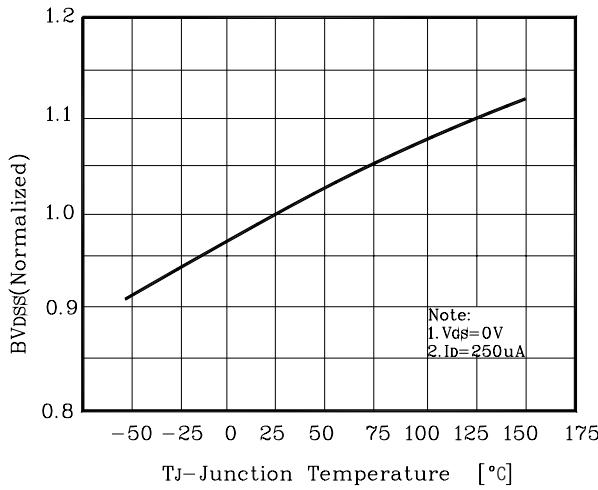


Fig. 8 R_{DS(on)} - T_J

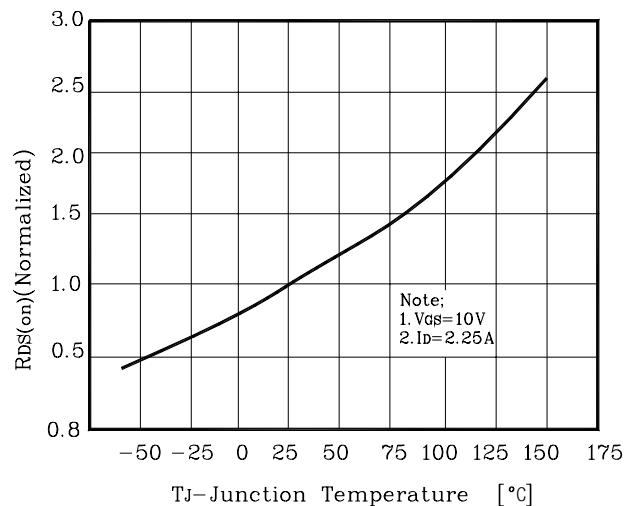


Fig. 9 I_D - T_C

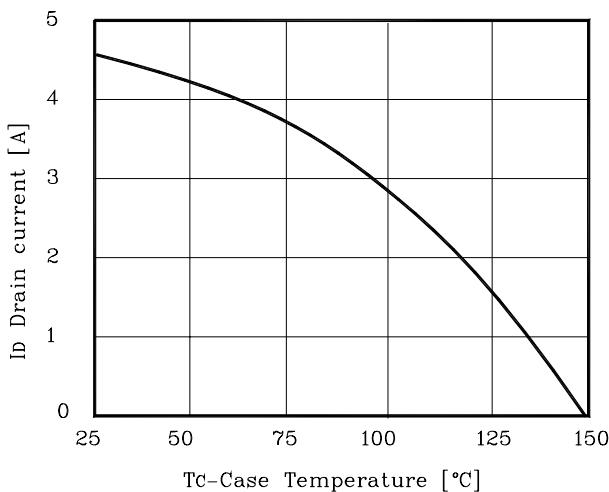


Fig. 10 Safe operating Area

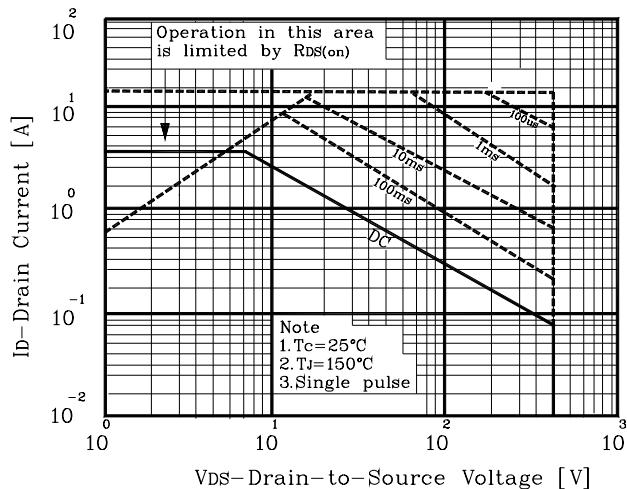


Fig. 11 Thermal Response

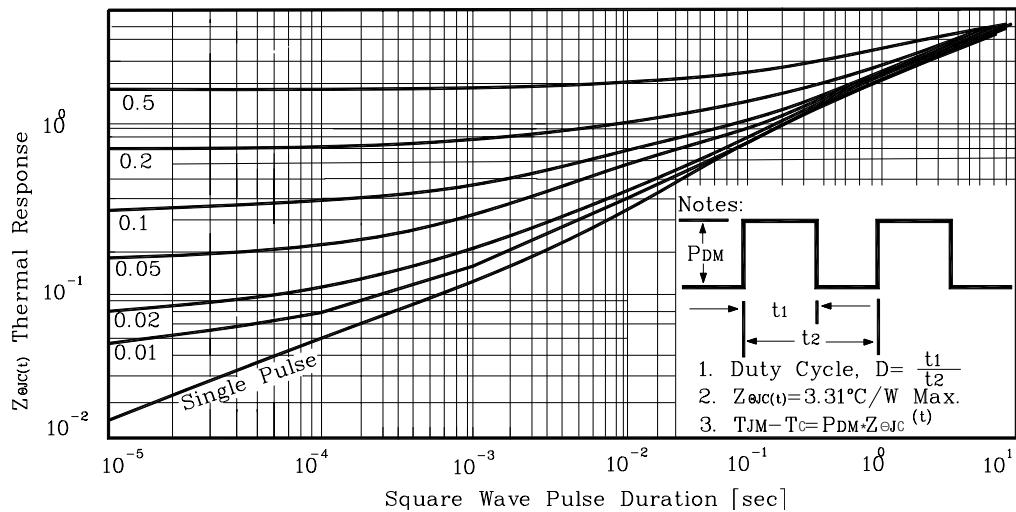


Fig. 12 Gate Charge Test Circuit & Waveform

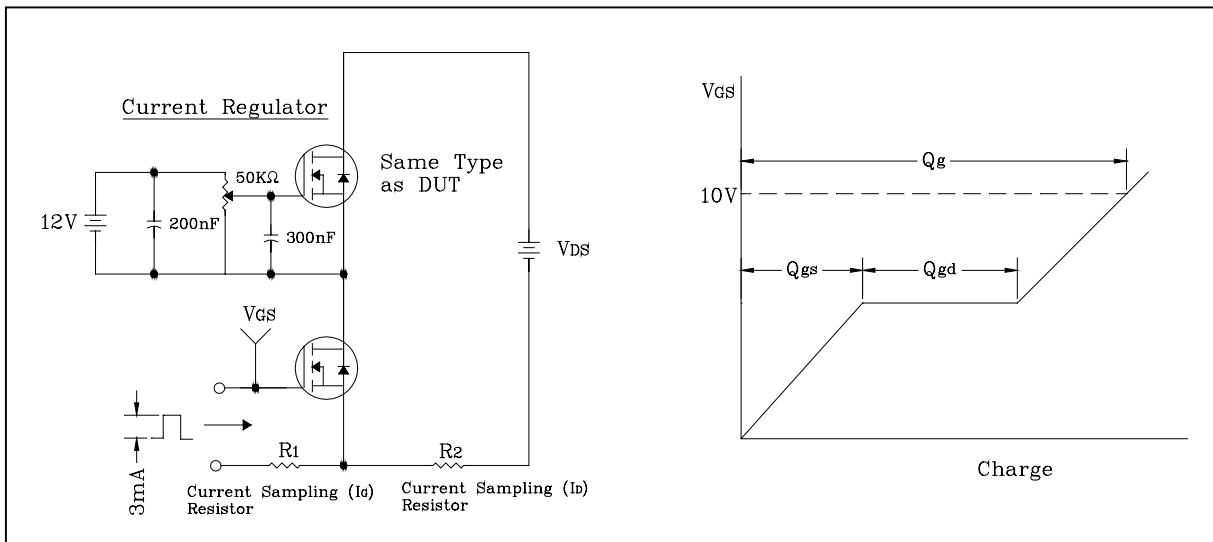


Fig. 13 Resistive Switching Test Circuit & Waveform

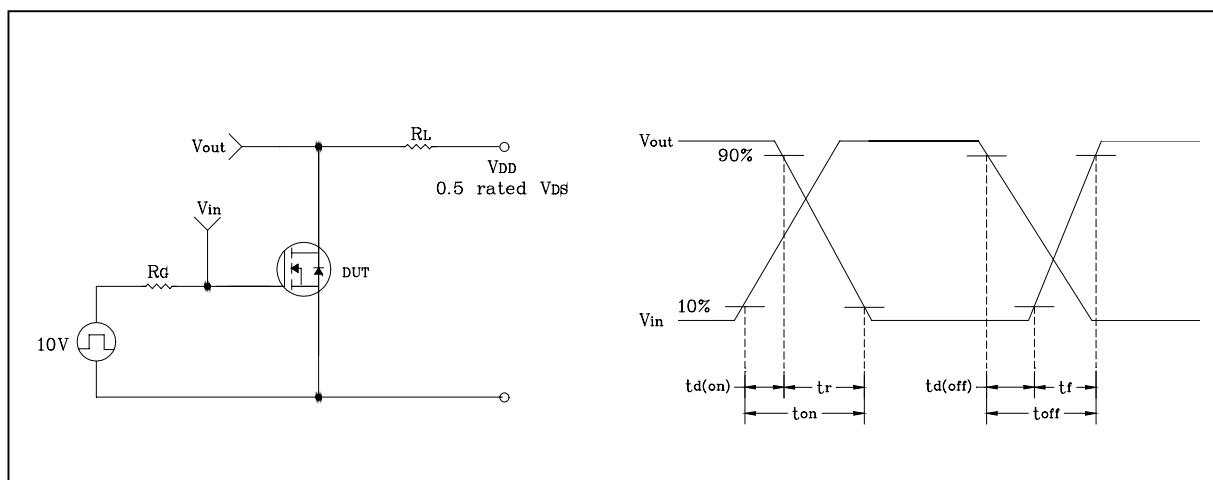


Fig. 14 Unclamped Inductive Switching Test Circuit & Waveform

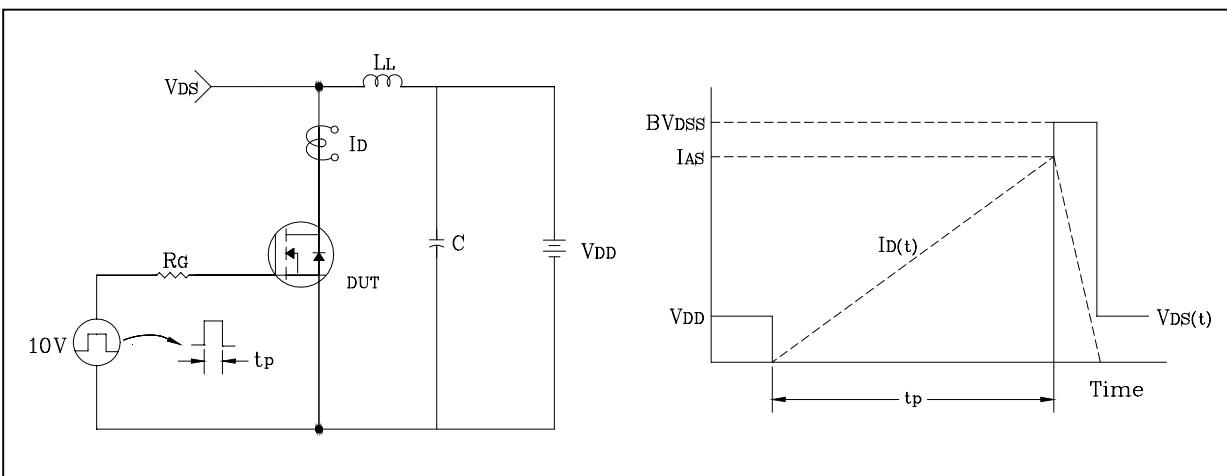
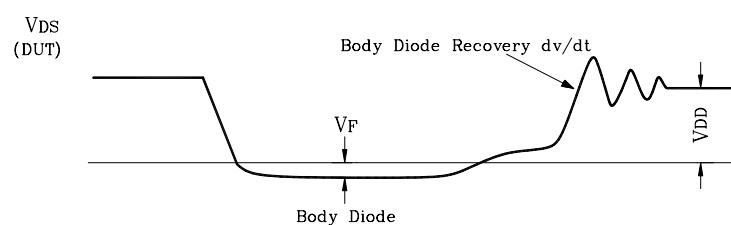
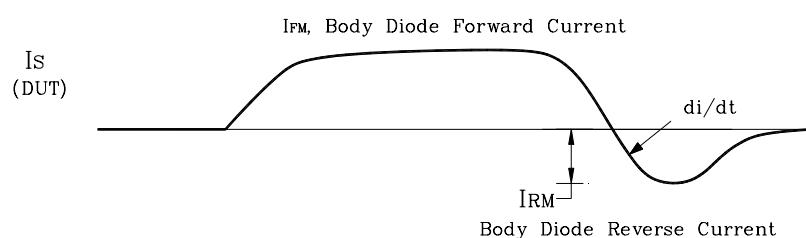
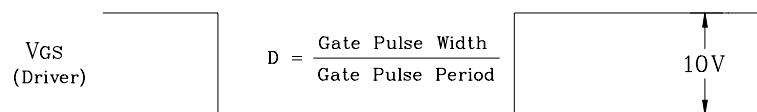
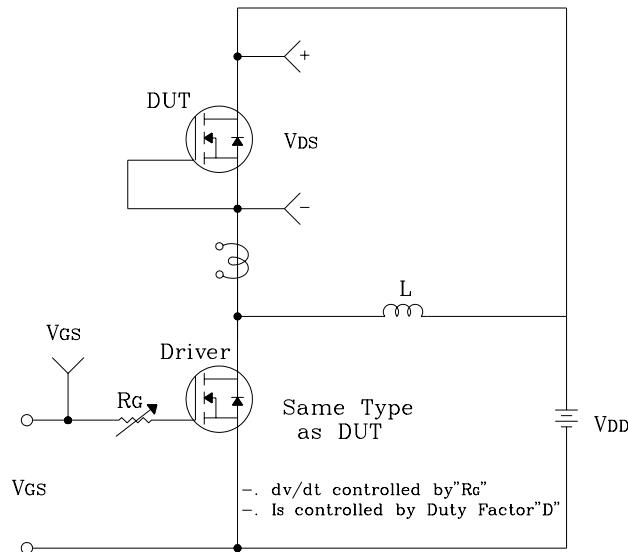


Fig. 15 Peak Diode Recovery dv/dt Test Circuit & Waveform



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